Ref. No. IKGPTU/T&P/.468.................      Dated..03/Dec/2025

**Directors/HoDs (Department of CSE/EE/ECE)**
**All the University Campuses**
**I K Gujral Punjab Technical University Jalandhar, Kapurthala**

**Sub: Semifront Technologies Pvt. Ltd. — Joint Campus Placement Drive.**

**Respected Sir/Madam**

I K Gujral Punjab Technical University invite students of its campuses to participate in Semifront Technologies Pvt. Ltd.- Joint Campus Placement Drive as per details mentioned below:-

| | | |
|---|---|---|
| Designation | : | **Profile 1:-** Sales & Marketing Intern **(Details attached)** <br> **Profile 2:-** RTL Design/Verification Engineer |
| Course/ Stream | : | **Profile 1:-** B.Tech/M.Tech (EE/ECE/CSE) <br> **Profile 2:-** B.Tech/M.Tech (ECE/EE) |
| Salary Package | : | **Profile 1:-** During 03 months internship:- 8k – 15k pm <br> **Profile 2:-** During 06 months internship:- 15k pm <br> After internship:- 3.50 LPA + additional bonus of 0.75 LPA based on interview performance (total ₹4.25 LPA). <br> 4.50 LPA after 1 year + probation + additional ₹0.75 LPA based on interview performance (total ₹5.25 LPA). <br> 5.50 LPA after 2 years + probation + potential bonus of 0-0.5 LPA based on interview performance (total 5.5-6 LPA). <br> Total Late Pay as additional bonus: 1.5 -2.00 Lakhs will be disbursed after completing 3 years with the company. |
| Job location | : | Sector V, Salt Lake City, Kolkata |
| Batch Eligible | : | 2025 passed out & 2026 passing out |
| Selection Process | : | Telephonic Interview, HR Round & Virtual interview |

**Interested students may register at the link mentioned below:-**

https://docs.google.com/forms/d/1Yi8BJcQmFgQGfvBz-Upf97zfOaD7C5odv8lytgxxpeI/edit

You are requested to kindly direct the Training & Placement Faculty Coordinator of your campus/department to share the information with the concerned students. **Last date of online registration is 05-Dec-25 before 1300 hrs.**

Date & Time of the selection process will be informed later on.

**With profound regards,**

Er. Mohit Jain
Assistant Registrar (T&P)

CC:

1. SVC: For kind information of the Hon'ble Vice Chancellor
2. Head (CT&P): For kind information
3. Deputy Director (T&P): For kind information
4. DR (ITS) - To upload at University website
5. File: ***"Propelling Punjab to a prosperous Knowledge Society"***

**Sales & Marketing Intern**
**Location:** Salt Lake City, Kolkata

**About Semifront**
 Semifront Technologies is an emerging semiconductor company specializing in PCIe technology and advanced silicon design solutions for global clients. With engineering operations in Salt Lake City, Kolkata, and a sales presence in the San Francisco Bay Area, Semifront is part of the FABCI at IIT Hyderabad initiative by MEITY, Government of India.

This internship is for final-year engineering students (about to graduate) who want to bridge technical expertise with business functions. The focus is on engineering-driven sales and marketing with supporting exposure to HR/Admin tasks.

**Role Overview**
 You will apply your engineering background to drive sales and marketing in the semiconductor and AI ecosystem while supporting HR/Admin activities.

- **80% focus:** Sales, Marketing & Technical Outreach
- **20% focus:** HR/Admin support

This is a desk-based role requiring structured execution with contributions to content, research, automation, and discussions that shape business outcomes.

**Key Responsibilities**

**Sales & Marketing (~80%)**

- Use AI-driven tools & metrics to run campaigns, track leads, and optimize outreach
- Research markets, competitors, and prospects in chip design & AI
- Create/adapt technical content, case studies, and documentation for customers and partners
- Support sales discussions, prepare presentations, and assist in product positioning
- Translate technical concepts into impactful customer messaging
- Explore light scripting/automation for data handling, reporting, or outreach

### HR & Administrative (~20%)

- Assist with recruitment tasks (job postings, coordination, scheduling)
- Support onboarding, compliance, and employee records
- Maintain HR trackers, prepare reports, and contribute to internal communication
- Handle occasional administrative coordination

### Why This Role is Ideal

- Mandatory **B.Tech OR M.Tech OR PhD** in **Electronics, ECE or Computer Science** (final-year, about to graduate)
- Strong entrance exam rank and academic performance preferred
- Engineering background gives credibility in technical sales/AI discussions
- Hands-on exposure to AI-driven research, sales metrics, and campaign analytics
- Opportunity to work on technical content, lead generation, and outreach
- Deep interest in solving problems with an engineering-driven approach
- Good performers have strong chances of being offered a **full-time role with competitive compensation**

**Stipend:** ₹8,000 – ₹15,000 per month

### What We Offer

- Mentorship in technical sales, marketing, and business operations
- Experience with AI-powered tools for sales & HR
- Opportunity to create impactful technical content
- Exposure to semiconductor industry networks & discussions
- Clear path toward full-time employment based on performance

## RTL Design/Verification Engineer

### Responsibilities:

- RTL and Testbench development using Verilog/SystemVerilog.

- Participating in the design and verification of complex ASIC/FPGA projects.

- Working closely with the design team to ensure thorough verification of designs.

- Debugging and analyzing functional issues at the RTL and gate levels.

### Selection Process:

1. Telephonic Interview with shortlisted candidates.

2. HR Round.

3. Virtual Technical Interview Round 1.

4. Virtual Technical Interview Round 2.