

## **IK GUJRAL PUNJAB TECHNICALUNIVERSITY, JALANDHAR**

### **Advertisement for the post of Research Fellow Full Time in the VISVESVARAYA Ph.D. Scheme for Electronics and IT: Phase II**

Applications are invited from eligible candidates for Two post of **Research Fellow (Full-Time)** in the **VISVESVARAYA Ph.D. Scheme for Electronics and IT: Phase II. Project and IT** at IKGPTU, Kapurthala. The details are as follows:

**Position :** Research Fellow (02) Full-Time

**Eligibility:** GATE/NET qualified candidates with ME/M-Tech in Electronics & Communication / Electronics & Instrumentation Engineering / Applied Electronics / Instrumentation & Control Engineering / Electrical & Electronics Engineering or equivalent specialized branches with minimum of 60% marks (or CGPA of 6.5 on 10-point Scale) in the qualifying examination i.e. ME/M-Tech.

**Desirable Qualifications /Experiences:** The Candidate from relevant background will be preferred.

**General terms and conditions:** Original documents of age proof /certificates /degree/mark-sheets and other testimonials must be presented at the time of interview. Self- attested photocopies of these documents are to be submitted along with be applications in the application from on the day of interview. **Eligible candidates can send the applications in the given format, including a cover letter and resume with detailed information about the educational qualifications, research experience and publications (if any ) in a single pdf file by email to dean\_rd@ptu.ac.in by 31<sup>st</sup>July 2024.** The Selection will be based on the performance on the interview, academic career, and the requirement of project. Mere possession of minimum qualification does not guarantee selection. Only selected candidate will be communicated to join as Research Fellow and no other communication in this regard will be entertained. The authority reserves the right of accepting/ rejecting any or all applications without assigning any reason thereof. **Selected candidate may be registered for Ph.D. program at IKGPTU, Kapurthala as per rules of IKGPTU/UGC.**

**The Shortlisted candidates may appear for the interview to be scheduled in the month of August 2024.**

**The information of interview will be communicated through email only provided in the application. No TA/DA will be provided for attending the interview.**

For more information, documents are also attached for kind reference:

(i) Implementation Order for the execution of VISVESVARAYA Ph.D. Scheme for Electronics and IT: Phase II at IKGPTU, Kapurthala

(ii) SOP & Terms and Conditions of vide no. IKGPTU/Reg/NF/R&D/158 dated 23.04.2024 under MeitY

(iii) Application form.