

PUNJAB TECHNICAL UNIVERSITY

Scheme& Syllabus of B. Tech. Electrical Engineering & Industrial Control [EIC]

Batch 2012

**By
Board of Studies Electrical Engineering**

Punjab Technical University

B.Tech Electrical Engineering & Industrial Control (EIC) Batch 2011

Note: There will be 04 weeks BTEE309 Institutional training after 2nd semester.

“S” for Satisfactory and US for unsatisfactory

Semester –III								
Course Code	Course Title	L	T	P	Marks Distribution		Total Marks	Credits
					Internal	External		
BTAM-301	Engineering Mathematics-III	4	1	-	40	60	100	5
BTEE-301	Circuit Theory	4	1	-	40	60	100	5
BTEE-302	Transformers & Direct Current Machines	4	1	-	40	60	100	5
BTEE-303	Electrical Measurements & Instrumentation	4	1	-	40	60	100	5
BTEE-304	Electronic Devices and Circuits	4	1	-	40	60	100	5
BTEE-305	Laboratory-I (Semiconductor Devices and Circuit Theory)	-	-	2	30	20	50	1
BTEE-306	Laboratory-II (Electrical Machines -I)	-	-	2	30	20	50	1
BTEE-307	Laboratory-III (Electrical Measurements)	-	-	2	30	20	50	1
BTEE-309	Institutional Training (Undertaken after 2 nd semester)				60	40	100	S/US
Total		20	5	6	350	400	750	28

Semester –IV								
Course Code	Course Title	L	T	P	Marks Distribution		Total Marks	Credits
					Internal	External		
BTEE-401	Asynchronous Machines	3	1	-	40	60	100	4
BTEE-402	Linear Control Systems	4	1	-	40	60	100	5
BTEIC-401	Industrial Instrumentation & Safety	3	1	-	40	60	100	4
BTEC-404	Digital Electronics	3	1	-	40	60	100	4
BTEE-405	Power System-I (Transmission & Distribution)	3	1	-	40	60	100	4
BTEE-406	Power Plant Engineering	3	1	-	40	60	100	4
BTEE-407	Laboratory-IV (Instrumentation & Measuring Devices)	-	-	2	30	20	50	1
BTEE-408	Laboratory-V (Control System)	-	-	2	30	20	50	1
BTEC-409	Laboratory-VI (Electronic Circuits)	-	-	2	30	20	50	1
General Fitness					100	-	100	S/US
Total		19	6	6	430	420	850	28

Punjab Technical University

B.Tech Electrical Engineering & Industrial Control (EIC) Batch 2011

Note: There will be 04 weeks BTEE509 Institutional training after 4th semester.

“S” for Satisfactory and US for unsatisfactory

Semester –V								
Course Code	Course Title	L	T	P	Marks Distribution		Total Marks	Credits
					Internal	External		
BTEE-501	Synchronous Machines	4	1	-	40	60	100	5
BTEIC-501	Process Dynamics and Control	4	1	-	40	60	100	5
BTEE-503	Microprocessors	4	1	-	40	60	100	5
BTEE-504	Power Electronics	4	1	-	40	60	100	5
BTEE-505	Numerical & Statistical Methods	4	1	-	40	60	100	5
BTEE-506	Laboratory-VII (Electrical Machines-II)	-	-	2	30	20	50	1
BTEE-507	Laboratory-VIII (Numerical Analysis)	-	-	2	30	20	50	1
BTEIC-502	Laboratory-IX (Process Control)	-	-	2	30	20	50	1
BTEE-509	Industrial Training (Undertaken after 4 th semester)				60	40	100	S/US
Total		20	5	6	350	400	750	28

Semester –VI								
Course Code	Course Title	L	T	P	Marks Distribution		Total Marks	Credits
					Internal	External		
BTEE-601	Electric Power Utilization	3	1	-	40	60	100	4
BTEE-602	Power System-II (Switch Gear & Protection)	3	1	-	40	60	100	4
BTEE-603	Non-Linear & Digital Control Systems	4	1	-	40	60	100	5
BTEE-604	Microcontroller and PLC	3	1	-	40	60	100	4
BTYY-6XX	Open Elective	3	1	-	40	60	100	4
BTEIC-605X	Elective-I	3	1	-	40	60	100	4
BTEE-606	Laboratory-X (Power Electronics & Drives)	-	-	2	30	20	50	1
BTEE-607	Laboratory-XI (Power System-I)	-	-	2	30	20	50	1
BTEE-608	Laboratory-XII (Micro controller & PLC)	-	-	2	30	20	50	1
General Fitness					100	-	100	S/US
Total		19	6	6	430	420	850	28

Semester –VII/VIII					
Course Code	Course Title	Marks Distribution		Total Marks	Credits
		Internal	External		
Industrial Training (One semester)					
BTEIC-701	Software Training	150	100	250	8
BTEIC-702	Industrial Oriented Project Training	300	200	500	10
Total		450	300	750	18

Semester –VII/VIII								
Course Code	Course Title	L	T	P	Marks Distribution		Total Marks	Credits
					Internal	External		
BTEIC-801	Advanced Process Control	3	1	-	40	60	100	4
BTEIC-802	Data Acquisition & Telemetry	4	1	-	40	60	100	5
BTEE-803	Non-conventional Energy Sources	3	1	-	40	60	100	4
BTEIC-804Y	Elective-II	3	1	-	40	60	100	4
BTEIC-805Z	Elective-III	3	1	-	40	60	100	4
BTEIC-802	Lab-XIII (Virtual Instrumentation)	-	-	2	30	20	50	1
BTEIC-807	Project Work	-	-	6	60	40	100	4
BTEIC-808	Seminar	-	-	2	100	-	100	2
General Fitness					100	-	100	S/US
Total		16	5	10	490	360	850	28

Elective	Electrical Power Systems	
Elective-I (BTEIC-605X)	BTEIC-605A	Electromagnetic Fields
	BTEIC-605B	Electric Generation & Economics
Elective-II (BTEIC-804Y)	BTEIC-804A	Power System Analysis
	BTEIC-804B	High Voltage Engineering
Elective-III (BTEIC-805Z)	BTEIC-805A	Power Quality Monitoring and Conditioning
	BTEIC-805B	Computer Aided Electrical Machine Design
Elective	Instrumentation and Control	
Elective-I (BTEIC-605X)	BTEIC-605C	Instrumentation in Power System
	BTEIC-605D	Biomedical instrumentation
Elective-II (BTEIC-804Y)	BTEIC-804C	Digital Signal Processing
	BTEIC-804D	Industrial Process Control
Elective-III (BTEIC-805Z)	BTEIC-805C	Virtual Instrumentation
	BTEIC-805D	Energy Efficient Machines
Elective	Electronics and Computers	
Elective-I (BTEIC-605X)	BTEIC-605E	Principles of Communication Systems
	BTEIC-605F	Microelectronics Technology
Elective-II (BTEIC-804Y)	BTEIC-804E	Networks and Data Communication
	BTEIC-804F	Data Mining and Pattern Recognition
Elective-III (BTEIC-805Z)	BTEIC-805E	Embedded Systems
	BTEIC-805F	Visual Programming

OPEN ELECTIVES – offered by Electrical Engineering & Industrial Control department to be studied by other discipline students except Electrical Engineering & Industrial Control and Electrical Engineering students

BTEIC-609	Virtual Instrumentation
BTEIC-610	Industrial Process Control

OPEN ELECTIVES –likely to be offered by other departments to be studied by Electrical Engineering & Industrial Control students

BTCE-6XX	Fluid Mechanics
BTHU-6XX	Entrepreneurships
BTME-6XX	Business Process and Re-engineering
BTHU-6XX	Human Resource Management
BTAM-6XX	Optimization Techniques
BTCS-6XX	Introduction to Business Systems
BTME-6XX	Total Quality Management

BTEE-301 CIRCUIT THEORY

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

CIRCUITS CONCEPTS: Independent and dependent sources, Signals and wave forms: Periodic and singularity voltages, step, ramp, impulse, doublet, loop currents and loop equations, node voltage and node equations, Network Theorems: Superposition, Thevenin's, Norton's, Maximum Power Transfer, and Reciprocity.

TIME AND FREQUENCY DOMAIN ANALYSIS: Representation of basic circuits in terms of generalized frequency and their response, Laplace transform of shifted functions, transient and steady response, Time domain behaviors from poles and zeros, Convolution Theorem.

NETWORK SYNTHESIS: Network functions, Impedance and admittance function, Transfer functions, Relationship between transfer and impulse response, poles & zeros and restrictions, Network function for two terminal pair network, Sinusoidal network in terms of poles & zeros, Real liability condition for impedance synthesis of RL & RC circuits, Network synthesis techniques for 2-terminal network, Foster and Cauer forms.

FILTERS SYNTHESIS: Classification of filters, characteristics impedance and propagation constant of pure reactive network, Ladder network, T-section, π -section, terminating half section, Pass bands and stop bands, Design of constant-K, m-derived filters, Composite filters.

RECOMMENDED BOOKS:

1. Bird John, *Electrical Circuit Theory and Technology*, 2nd Ed., Newnes
2. Chakraborty, Abhijit, *Circuit Theory*, 2nd Edition, Dhanpat Rai, 2001
3. Chaudhury D. Roy, *Networks & Synthesis*, New Age International.
4. Edminister J.A., *Electric Circuits*, 4th Edition, Tata McGraw Hill, 2002
5. Iyer T.S.K.V., *Circuit Theory*, Tata McGraw Hill, 2006
6. Mohan, Sudhakar Sham, *Circuits & Networks Analysis and Synthesis*, 2nd Edition, Tata McGraw Hill, 2005
7. Van Valkenberg, M.E., *Network Analysis & Synthesis*, PHI learning, 2009
8. Van Valkenberg, M.E., *Network Analysis & Synthesis*, 3rd Edition, Pearson Education, 2006

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-302 TRANSFORMERS AND DIRECT CURRENT MACHINES

Internal Marks:	40	L	T	P
External Marks:	60	4	1	0
Total Marks:	100			

TRANSFORMERS: Working principle, construction of single phase transformer, EMF equation, phasor diagrams on no-load and on loaded conditions, open circuit and short circuit tests, equivalent circuit parameters estimation, voltage regulation and efficiency, back to back test. Effect of saturation on exciting current and in-rush current phenomenon. Parallel operation of single phase transformers.

AUTO TRANSFORMERS: Principle of operation, equivalent circuit and phasor diagrams, comparison with two winding transformer.

THREE-PHASE TRANSFORMERS: Different types of winding connections, Voltage and current ratios, Parallel operation of three phase transformers. Three winding transformer's equivalent circuit, off-load and on-load tap changing transformer, Scott connections. Testing of transformers.

D.C. GENERATOR: Working principle, construction of DC Machines, Armature windings, single and double layer winding diagrams, E.M.F. and torque equations, armature reaction, effect of brush shift, compensating winding, commutation, causes of bad commutation, methods of improving commutation, methods of excitation of d.c. generators and their characteristics.

D.C. MOTOR: Working principle characteristics, starting of shunt and series motor, starters, speed control methods: field and armature control. Braking: plugging, dynamic and regenerative braking, Testing: Swinburn's test, Hopkinson test, Field test. Estimation of losses and efficiency.

BOOKS RECOMMENDED:

1. Bimbhra P.S., *Electrical Machinery*, Khanna Publishers
2. Fitzgerald A.E., Kingsley C. and Umans S.D., *Electric Machinery*, 6th Edition, McGraw Hill
3. Langsdorff E.H., *Principles of D.C. machines*, McGraw Hill
4. Nagrath I.J. and Kothari D.P., *Electrical Machines*, 4th Edition, Tata McGraw Hill,
5. Say M G, *Alternating Current Machines*, 5th edition, Sir Isaac Pitman & Sons Ltd.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-303 ELECTRICAL MEASUREMENTS AND INSTRUMENTATION

Internal Marks:	40	L	T	P
External Marks:	60	4	1	0
Total Marks:	100			

UNITS, DIMENSIONS AND STANDARDS: Introduction to MKS & Rationalized MKSA System, SI Units, Standards of EMF, Resistance, Capacitance and Inductance, Systematic errors

GENERAL THEORY OF ANALOG MEASURING INSTRUMENTS: Operating torque, damping & controlling torque, T/W ratio, Pointers & Scales. Principles of operation of various types of electro mechanical indicating / registering instruments viz. PMMC, dynamometer, induction, thermal, etc. for dc & ac measurement of voltage, current, power, frequency, phase & power factor etc., energy meter: their sources of error & compensation, shunts & multipliers, multi-meter.

POTENTIOMETERS: Basic D.C. potentiometer circuit, Modern form of D.C. potentiometer, measurement of voltage, current, Resistance and calibration of voltmeter & ammeter using D.C. potentiometer, volt ratio box, Self balancing potentiometer, A.C. potentiometers and their applications.

BRIDGES: Sources and Detectors, General equation for bridge balance, Wheatstone bridge and its sensitivity analysis, Kelvin double bridge, AC bridges: applications and conditions for balance, Maxwell's bridge, Hay's bridge, Schering bridge, Wien bridge, DeSauty's bridge, Insulation testing, Sources of errors in bridge circuits, Shielding of bridge elements, Wagner Earthling Device.

MAGNETIC MEASUREMENTS: Flux meter, B-H Curve, Hysteresis loop, Permeameters, AC Testing of Magnetic materials, Separation of iron losses, iron loss measurement by Wattmeter and Bridge methods.

INSTRUMENT TRANSFORMERS: Theory and construction of current and potential transformers, ratio and phase angle errors and their minimization, Characteristics of current transformers (CT).and potential transformers (PT). and their Testing.

BOOKS RECOMMENDED:

1. Bell David A., *Electronics Instrumentation and Measurements*, Prentice Hall, India
2. Golding Edward William and Widdis Frederick Charles, *Electrical Measurements and Measuring instruments*, Wheelers India
3. Helfrick A.D. and Cooper W.D., *Modern Electronic Instrumentation. & Measurement Techniques*, Prentice Hall
4. Murthy D. V. S., *Transducers and Instrumentation*, Prentice-Hall, India
5. Sawhney A. K., *A Course in Electrical & Electronics Measurement & Instrumentation.*, Dhanpat Rai & Sons.
- 6.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-304 ELECTRONIC DEVICES AND CIRCUITS

Internal Marks:	40	L	T	P
External Marks:	60	4	1	0
Total Marks:	100			

BASIC SEMICONDUCTOR AND DIODES: Intrinsic and extrinsic semiconductors, diffusion and drift currents, p-n junction under open-circuit, reverse bias and forward-bias conditions, p-n junction in the breakdown region, Ideal diode, terminal characteristics of junction diode, Load-line analysis of diode circuits, half wave rectifier and full wave rectifiers, Clippers and Clampers, capacitive filters, RC and LC filter, voltage multipliers. Principles, construction, characteristics and applications of Zener diodes, Light Emitting Diodes, Schottky Diode, Varactors

BIPOLAR AND UNIPOLAR TRANSISTORS: Bipolar junction transistor (BJT)- physical structure and modes of operation, Transistor characteristic and parameters, Common Base, Common Emitter and Common Collector Configurations, Transistor biasing, Transistor as a switch, Basics characteristics of an amplifier, Simple transistor model (r_e model), Common Emitter, Common Collector and Common base amplifiers, hybrid equivalent circuit, H-parameters, circuit analysis using h-parameters. Junction field effect transistor (JFET): Characteristics, parameters and biasing. Metal oxide field effect transistor (MOSFET): Characteristics, parameters and biasing. Class A power amplifier, Class B, Class AB Push-pull and Class C amplifiers.

INTEGRATED CIRCUIT AND OPERATIONAL-AMPLIFIERS: Introduction to IC's, Op-Amps, Op-Amp Characteristics, Feedback, Different feedback configurations, Current– to–voltage converter and voltage-to-current converters, voltage and current amplifiers, mathematical operations using Op-Amp, summing, differential, integrating amplifiers, Comparators and Schmitt trigger

OSCILLATORS AND ACTIVE FILTERS: Oscillations, Feedback oscillator Principles,, RC phase shift oscillator, Wein bridge oscillator, Hartley oscillator, Colpitts oscillator, Crystal oscillators, frequency stability, negative resistance in oscillators. Active Filters (1st order) with low pass, high pass, band pass, band stop and all pass. Pin configuration of 555 timer, 555 timer as Oscillator: monostable, bistable and astable multivibrator.

REGULATED POWER SUPPLIES: Unregulated power supplies, line and load regulations, Zener diode voltage regulators, transistor series and shunt regulators, current limiting, Op-Amp voltage regulators, integrated circuit (LM-3XX) voltage regulators. Introduction to switching regulators. Working of Switched Mode Power Supply (SMPS).

BOOKS RECOMMENDED

1. Boylestad, Robert.L. *Electronic Devices and Circuit Theory*, Pearson Education
2. Cathey Jimmie J., *Theory and Problems of Electronic Devices and Circuits*, McGraw-Hill
3. Floyd Thomas L., *Electronic Devices*, Pearson Education
4. Gayakwad, Ramakant A. *OP-AMPS and Linear Integrated Circuits*, Prentice Hall of India
5. Malvino Albert Paul and Bates David, *Electronic Principles*, edition 7th, Tata McGraw Hill
6. Millman Jacob, *Integrated Electronic Devices and Circuits*, Tata McGraw Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-305 Laboratory-I (Semi-conductor Devices and Circuit Theory)

Internal Marks:	30	L	T	P
External Marks:	20	0	0	2
Total Marks:	50			

List of Experiments:

1. To draw V-I characteristics of PN junction diode (Ge, Si, switching and signal).
2. To design half wave rectifier.
3. To design full wave and bridge rectifiers.
4. To study transistor characteristics in common base and common emitter configurations.
5. To study the FET characteristics.
6. To design, study and compare various transistor biasing techniques.
7. To design regulated power supply using zener diode/ voltage regulator IC.
8. To study of an emitter follower circuit.
9. To verify Superposition theorem.
10. To verify Norton's theorem.
11. To verify Thevenin's theorem.
12. To verify maximum power transfer theorem.
13. To study the response of constant K-filters.
14. To study the response of m-derived filters
15. Diode clippers and clampers.

BTEE-306 Laboratory-II (Electrical Machines-I)

Internal Marks:	30	L	T	P
External Marks:	20	0	0	2
Total Marks:	50			

List of Experiments

1. To Load test on a single phase transformer.
2. To perform Open circuit and short circuit tests on a single phase transformer and hence find equivalent circuit, voltage regulation and efficiency.
3. To find the efficiency and voltage regulation of single phase transformer under different loading conditions.
4. To perform parallel operation of two single phase transformers.
5. To study the various connections of three phase transformer.
6. To perform Scott connections on three phase transformer to get two phase supply.
7. To study the constructional details of direct current (d.c.) machine and to draw sketches of different components.
8. To measure armature and field resistance of direct current (d.c.) shunt generator and to obtain its open circuit characteristics.
9. To obtain load characteristics of direct current (d.c.) shunt/series /compound generator.
10. To draw speed-torque characteristics of direct current (d.c.) shunt/series /compound generator.
11. To study direct current (d.c.) motor starters.
12. To perform Swinburne's test (no load test) to determine losses of direct current (d.c.) shunt motor.

BTEE-307 Laboratory-II (Electrical Measurements)

Internal Marks:	30	L	T	P
External Marks:	20	0	0	2
Total Marks:	50			

List of experiments:

1. Study of principle of operation of various types of electromechanical measuring instruments.
2. a) To measure high value of DC current by a Low Range DC Ammeter and Shunt.
b) To measure high value of DC voltage by a Low Range DC Voltmeter and Multiplier
3. a) To measure high value of AC Current by a Low Range AC Ammeter and Current Transformer.
b) To measure high value of AC Voltage by Low Range Voltmeter and Potential Transformer
Measurement of resistance using Wheatstone Bridge.
4. To measure active and reactive power in 3 phase balanced load by one wattmeter method.
5. To measure the active power in three phase balanced and unbalanced load by two wattmeter method and observe the effect of power factor variation on wattmeter reading.
6. To calibrate and use the Induction Energy Meter.
7. Measurement of resistance using Kelvin's Bridge.
8. Measurement of self inductance using Anderson's Bridge.
9. Measurement of capacitance using Schering Bridge.
10. Plotting of Hysteresis loop for a magnetic material using flux meter.
11. Measurement of frequency using Wein's Bridge.
12. To study the connections and use of Current and potential transformers and to find out ratio error.
13. Determination of frequency and phase angle using CRO.
14. Measurement of unknown voltage using potentiometer.
15. To find 'Q' of an inductance coil and verify its value using Q- meter.

BTEE-401 ASYNCHRONOUS MACHINES

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

POLYPHASE INDUCTION MACHINES: Analogy between induction motor and transformer, production of rotating field in space distributed three-phase winding, constructional features, concept of slip and operation, rotor frequency, current and power, equivalent circuit, phasor diagram, torque-slip characteristics, effect of rotor circuit resistance, starting torque, crawling and cogging, cage motors(double cage and deep bar motor).

STARTING METHODS AND SPEED CONTROL: Starting methods, speed control: (i) control of speed of rotating field, (ii) control of slip speed. Effect of voltage injection in rotor circuit of slip ring induction motor. Motor tests for estimation of equivalent circuit parameters.

INDUCTION GENERATOR: Isolated and Grid mode operation, method of excitation, performance characteristics of three-phase self-excited induction generator.

SPECIAL PURPOSE MOTORS: Stepper Motors: construction, principle of operation and applications. Linear Induction Machines: construction, principle of operation and applications. Universal Motor: construction, principle of operation and applications.

SINGLE –PHASE MOTORS: Double revolving field theory, types of single phase motors, characteristics and equivalent circuit. Shaded pole motor: working principle and characteristics.

BOOKS RECOMMENDED:

1. Fitzgerald A.E., Kingsley C. and Umans S.D., *Electric Machinery*, 6th Edition, McGraw Hill
2. Langsdorff E.H., *Principles of A.C. Machines*, McGraw Hill
3. Nagrath I.J. and Kothari D.P., *Electrical Machines*, 4th Edition, Tata McGraw Hill,
4. Bimbhra P.S., *Electrical Machinery*, Khanna Publishers
5. Say M G, *Alternating Current Machines*, 5th edition, Sir Isaac pitman & Sons Ltd.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-402 LINEAR CONTROL SYSTEMS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

INTRODUCTORY CONCEPTS: Plant, Systems, Servomechanism, regulating systems, disturbances, Open loop control system, closed loop control systems, linear and non-linear systems, time variant and invariant, continuous and sampled-data control systems, Block diagrams, some illustrative examples.

MODELING: Formulation of equation of linear electrical, mechanical, thermal, pneumatic and hydraulic system, electrical, mechanical analogies. Use of Laplace transforms, Transfer function, concepts of state variable modeling. Block diagram representation, signal flow graphs and associated algebra, characteristics equation.

TIME DOMAIN ANALYSIS: Typical test – input signals, Transient response of the first and second order systems. Time domain specifications, Dominant closed loop poles of higher order systems. Steady state error and coefficients, pole-zero location and stability, Routh-Hurwitz Criterion.

ROOT LOCUS TECHNIQUE: The extreme points of the root loci for positive gain. Asymptotes to the loci, Breakaway points, intersection with imaginary axis, location of roots with given gain and sketch of the root locus plot.

FREQUENCY DOMAIN ANALYSIS: Closed loop frequency response, Bode plots, stability and loop transfer function. Frequency response specifications, Relative stability, Relation between time and frequency response for second order systems. Log. Magnitude versus Phase angle plot, Nyquist criterion for stability.

COMPENSATION: Necessity of compensation, series and parallel compensation, compensating networks, applications of lag and lead-compensation.

CONTROL COMPONENTS: Error detectors – potentiometers and synchros, servo motors, a.c. and d.c. techno generators, Magnetic amplifiers.

BOOKS RECOMMENDED

1. Dorf Richard C. and Bishop Robert H., *Modern Control System*, Addison –Wesley, Pearson New Delhi
2. Ogata K., *Modern Control Engineering*”, Prentice Hall,
3. Kuo B. C., *Automatic Control System*”, Prentice Hall
4. Nagrath I.J. and Gopal M., *Control System Engineering*, Wiley Eastern Ltd.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-401 INDUSTRIAL INSTRUMENTATION & SAFETY

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

Temperature Measurement: Transducers used for temperature measurement, Temperature scale and conversion, principle of operation, construction, characteristics of vapor, gas, liquid filled thermo meters, thermocouple and its configuration, extension wires, bimetallic thermo meter, resistance temperature detector & compensation techniques, Thermistor, pressure spring thermometer, radiation and optical pyrometer.

Pressure Measurement: Transducers used for pressure measurement, Introduction to static and dynamic pressure, unit of pressure and conversions, pressure standards, principle of working, material of Construction, advantages and limitations of various instruments based upon elastic transducers like ordinary and diaphragm, bourdan tube, bellows, capsules etc. sealed pressure gauges, pressure transmitters, pressure switches and strain gauge pressure pick ups, methods for measurement of vacuum and high pressure. Piratti Gauge, calibration of pressure measurement, Mclead Gauge, K Gauge.

Flow Measurement: Transducers used for flow measurement, Basic properties of fluids, principle of operation. construction, classification, characteristics of various measurement methods, variable headmeters, orifice plate & its types, venturi tube, flow nozzle, pitot tube, rotameter and its types, magnetic meter, turbine meter, vortex meter, mass flow meter. ultrasonic meter, thermal flow meter. Positive displacement meters.

Level Measurement: Transducers used for level measurement, Importance of level measurement, principle of working, material of construction, advantage and limitation of various instruments such as visual level indicators, ordinary float type using string and pulley, purge method of measuring level Buoyancy method, resistance probes for level measurement, capacitive level meter, ultrasonic level measurement, Gamma rays level measurement, level limit switches, level measurement of closed vessel.

Industrial Safety: Fire Prevention and Control, Handling of Fire Accidents, Electrical Safety, Environmental Safety, Various safety equipments and their constructional features, Maintenance and repair of safety equipments, Safety in High Pressure Operations, Safety Management.

BOOKS RECOMMENDED:

1. Jhonson, *Process Control Instrument*, Prentice Hall (India)
2. Kallan, *Instrumentation Handbook*, Tata McGraw Hill
3. Nakara, *Instrument Measurement & Analysis*, Prentice Hall (India)
4. E.O. Deoblin, *Measurement Systems Applications*, Mc Graw Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEC- 404 DIGITAL ELECTRONICS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

NUMBER SYSTEM & CODES: Binary number system, octal number system, hexadecimal number system, BCD Code, Gray code, signed & unsigned binary numbers, 1's & 2's complement of a number, different types of codes, Binary operations- addition, subtraction, multiplication, division, Parity for error detection, Check sum and Hamming Code for error detection and correction.

COMBINATIONAL CIRCUITS: Concept of positive and negative logic, Introduction to Boolean variables, Boolean theorems and DeMorgan Theorem, Sum of product and Product of sum form of Logic expressions, Duality, Logical functions using Karnaugh map and Quine-McClusky methods, multiplexers, demultiplexers, encoders, decoders, adders, subtractors, parity generators, parity checkers, code converters.

SEQUENTIAL LOGIC CIRCUITS: Flip-flops, JK flip-flops, D flip-flops, T flip-flops, SR flip-flops, edge triggered and clocked flip-flops. Registers and Counters: Series and Parallel registers; Synchronous & Asynchronous counters, Up and Down counters, Ring counters & Mod- Counters.

INTRODUCTION TO VHDL: Overview of digital design with very-high-speed integrated circuits (VHSIC) hardware description language (VHDL), HDL format and Syntax, entity, Data representation in VHDL, Truth table using VHDL, Decision Control structure and Sequential Circuit using VHDL.

DIGITAL LOGIC FAMILIES: Introduction, characteristics of digital ICs, resistor-transistor logic, integrated-injection logic, direct-coupled transistor logic, diode-transistor logic & transistor-transistor logic, emitter-coupled logic and MOS logic

DIGITAL TO ANALOG (D/A) AND ANALOG TO DIGITAL (A/D) CONVERTERS: Introduction, weighted register D/A converter, binary ladder, D/A converter, specifications for D/A converters, parallel A/D converter, successive approximation A/D converter single & dual slope A/D converter, A/D converter using voltage to frequency conversion, A/D converter using voltage to time conversion, countertype A/D converters.

SEMICONDUCTOR MEMORIES: Introduction, memory organization, classification & characteristics of memories, sequential memories, read only memories, read & write memories, content addressable memories, Programmable array Logic, programmable logic arrays and Programmable Logic Device, Field Array Programmable Gate array

RECOMMENDED BOOKS:

1. Floyd Thomas S. *Digital Fundamentals*, Pearson Education
2. Jain R.P., *Modern digital Electronics*, Tata McGraw Hill
3. Kumar Anand, *Fundamentals of Digital Circuits*, Prentice Hall of india
4. Malvino Albert Paul, *Principles of Digital Electronics*, Tata McGraw Hill
5. Mano Morris, *Digital Logic and Computer Design*, Prentice Hall of India
6. Tocci Ronald J. Widmer Neal S. and Moss Gregory L., *Digital Systems: Principles and Applications*, Prentice Hall of India

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-405- POWER SYSTEMS – I (Transmission and Distribution)

Internal Marks: **40**
External Marks: **60**
Total Marks: **100**

L	T	P
3	1	0

SUPPLY SYSTEM: Introduction to Transmission and Distribution systems, Comparison between DC and AC systems for Transmission and Distribution, comparison of cost of conductors, choice of working voltage for transmission and distribution, economic size of conductors - Kelvin's law, Radial and mesh distribution networks, Voltage regulation.

CONDUCTORS AND TRANSMISSION LINE CONSTRUCTION: Conductor materials; solid, stranded, ACSR, hollow and bundle conductors. Different types of supporting structures for overhead lines. Elementary ideas about transmission line construction and erection. Stringing of conductors, spacing, sag and clearance from ground, overhead line insulators, concept of string efficiency.

TRANSMISSION LINE PARAMETERS: Introduction to line parameters, Resistance of transmission line, inductance of single phase two wire line, concept of G.M.D., Inductance of three phase line, Use of bundled conductor, transposition of power lines, capacitance of 1-phase and 3-phase lines. effect of earth on capacitance of conductors.

PERFORMANCE OF TRANSMISSION LINES: Representation of short transmission line, medium length line (nominal T & II circuits). long length line by hyperbolic equations and equivalent T & II circuits. Power flow through transmission lines, ABCD constants, Voltage regulation.

CIRCLE DIAGRAM AND LINE COMPENSATION: Receiving end circle diagram for long transmission lines based on ABCD constants, equivalent T circuits, power loci, surge impedance loading, reactive power requirement of system series and shunt compensation, Synchronous phase modifiers , rating of phase modifiers.

UNDERGROUND CABLES: Classification of cables based upon voltage and dielectric material, insulation resistance and capacitance of single core cable, dielectric stress, Capacitance of 3 core cables, methods of laying, heating effect, Maximum current carrying capacity, cause of failure, comparison with overhead transmission lines.

RECOMMENDED BOOKS

1. Elgerd O.L., *Electrical Energy System Theory - An introduction*, Tata McGraw-Hill Publication
2. Gupta B.R., *Power System Analysis & Design*, Wheeler Publishing.
3. Nagrath I.J. and Kothari D.P., *Power System Analysis* Tata McGraw-Hill Publication
4. Stevenson Jr. W.D., *Elements of Power System Analysis*, Tata McGraw-Hill Publication
5. Wadhwa C.L., *Course in Electrical Power*, New Age International (P)Ltd.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-406 POWER PLANT ENGINEERING

Internal Marks:	40	L	T	P
External Marks:	60	3	1	0
Total Marks:	100			

STEAM GENERATORS, CONDENSERS AND TURBINES: Classification of steam generators, selection, operation of locomotive, Babcock Wilcox, Cochran boilers, Types of condensers, effect of air in condensers, Dalton's law of partial pressure, cooling water calculations, steam nozzles, types of steam turbine efficiencies, compounding, governing and control.

STEAM POWER PLANT: Classification, Operation, Description of Rankin cycle, Regenerative cycle, Reheat-Regenerative Cycle, Binary Vapour Cycle, Selection of plant site and its layout, coal handling system, combustion system, Fluidised bed combustion, Ash handling, Feed pumps, Heat exchangers, Economizers, Super heaters, Reheaters, Air preheaters, Feed water heaters, Evaporators.

HYDRO-ELECTRIC POWER PLANTS: Hydrological Cycle, Hydrograph, Flow duration curve, Selection of site, Essential features, Classification of hydro plants, Selection of water turbines for hydro power plant, Automatic and remote control of hydro-station, layout of hydro power plant.

NUCLEAR POWER PLANTS: Nuclear physics, Binding energy, Radioactive decay. Fertile material, Mass defect, Nuclear reactions type and application, Generation of nuclear energy by fission, Nuclear reactors. Site selections, safety measures, plant layout, Fusion reaction, Future of nuclear power.

GAS TURBINE: Elements of gas turbines, Open and closed cycles for gas turbines, Performance terms, Thermal refinement to gas turbines cycle, Plant layout, applications, gas turbines Cycle calculations.

DIESEL POWER PLANTS: Classifications of IC Engines and their performance, Four stroke and two stroke diesel engines, combustion phenomenon; Essential components, Celane number, knocking, super charging, operation and layout of diesel power plant.

COMBINED OPERATION OF DIFFERENT POWER PLANTS: Advantages of combined operation of plants, load division between power stations, coordination of different types of Power Plants.

POLLUTION CONTROL: Pollution from thermal & nuclear plants, Particulate emission and control, electrostatic precipitator, solid waste disposal.

RECOMMENDED BOOKS:

1. Chakrabarti A., Soni, M.L. Gupta P.V. and Bhatanagar U.S., *A Textbook on Power System Engineering*, Dhanpat Rai & Co.
2. EI-Wakit M.M., *Power Plant Engineering*, McGraw Hill, USA
3. Rajput R.K., *Power Plant Engineering*, Luxmi Publications
4. Sharma P.C., *Power Plant Engineering*, Kataria & Sons
5. Skrotzki B.G.A. and Vapot W.A., *Power Station Engineering and Economy*, Tata McGraw-Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-407 Laboratory-IV (Instrumentation & Measuring Devices)

Internal Marks: 30
External Marks: 20
Total Marks: 50

L	T	P
0	0	2

List of Experiments:

1. To study the input-output characteristics of a potentiometer and to use a potentiometer as an error detector.
2. To measure Insulation Resistance by Megger.
3. To measure earth resistance by Earth Tester.
4. To observe phase sequence of three phase circuit using Rotating type Phase Sequence Indicator.
5. To measure frequency of A.C. supply using Weston Frequency Meter.
6. To measure power factor of single phase and three phase load by PF Meter and verifying through current, voltage and power measurement.
7. To measure circuit parameters and three phase load by PF Meter by LCR Meter.
8. Measurement of displacement using LVDT.
9. Temperature measurement using temperature sensor (RTD).
10. Light measurement using LDR and photo cell sensor.
11. Water level measurement using capacitance transducer of a Liquid in a Tank
12. Velocity measurement using air flow transducer.
13. RPM measurement using electromagnetic transducers.
14. Study of the characteristics of a Piezoresistive Sensor for Pressure Measurement of a Liquid in a Tank
15. Study of the characteristics of Resistance Temperature Detector(RTD)
16. Study of the characteristics of a Thermistor
17. Study of the characteristics of a Thermocouple
18. Study of the characteristics of an Electromagnetic Flowmeter
19. Study of the characteristics of a Tachometer
20. Study of the characteristics of a Photo reflective sensor for Speed Measurement

BTEE-408 Laboratory-V (Control System)

Internal Marks: **30**
External Marks: **20**
Total Marks: **50**

L	T	P
0	0	2

List of Experiments:

1. To study the characteristics of potentiometers and to use 2- potentiometers as an error detector in a control system.
2. To study the synchro Transmitter-Receiver set and to use it as an error detector
3. To study the Speed – Torque characteristics of an AC Servo Motor and to explore its applications.
4. To study the Speed – Torque characteristics of an DC Servo Motor and explore its applications.
5. To study various electro-mechanical transducers i.e. resistive, capacitive and inductive transducers
6. To study a LVDT (AC-AC, DC-DC) as a transducer and its processing circuits
7. To study the characteristics of a thermocouple, a thermistor and a RTD
8. To study photo-conductive cell, semi-conductor photodiode and a silicon photo voltaic cell
9. To study a silicon phototransistor and obtain response of photo conductive cell
10. To study the variations of time lag by changing the time constant using control engineering trainer
11. To simulate a third order differential equations using an analog computer and calculate time response specifications
12. To obtain the transfer function of a D.C. motor – D.C. Generator set using Transfer Function Trainer
13. To study the speed control of an A.C. Servo Motor using a closed loop and an open loop systems
14. (i) To study the operation of a position sensor and study the conversion of position in to corresponding voltage
(ii) To study an PI control action and show its usefulness for minimizing steady state error of time response.
15. To measure Force / Displacement using Strain Gauge in a wheat stone bridge
16. To design a Lag compensator and test its performance characteristics.
17. To design a Lead-compensator and test its performance characteristics.
18. To design a Lead-Lag compensator and test its performance characteristics.

Note: At least 10 Experiments, out of above list of experiments are to be performed in the semester.

BTEC-409 Laboratory-VI (Electronic Circuits)

Internal Marks: 30
External Marks: 20
Total Marks: 50

L	T	P
0	0	2

List of Experiments:

1. To design a voltage regulator using zener diode and also see the effect of line and load regulation
2. To design various clippers and clampers using diodes.
3. To obtain the frequency response of an amplifier and calculate the gain bandwidth of the amplifier.
4. To investigate the emitter follower (Buffer) amplifier and determine A_v, R_i, R_o
5. To study the characteristics of a class B amplifier and also calculate the overall efficiency.
6. To study the characteristics of a class AB amplifier.
7. To study the characteristics symmetry amplifier.
8. To design and study various type of oscillators and to determine the frequency of oscillations.
9. To design a transistor series voltage regulator with current limits and observes current feedback characteristics.
10. To study the characteristics of a complementary symmetry amplifier.
11. Application of Op-Amp(741) as inverting and non-inverting amplifier.
12. To use the OP-AMP as summing, scaling and averaging amplifier.
13. Design differentiator and integrator using OP-AMP and also determine the time constant and cut-off frequency.
14. Application of OP-AMP as Schmitt Trigger.
15. Design a delay circuit using 555 timer and study the monostable, bistable and astable operations using 555.
16. a) Verification of the truth tables of TTL gates viz; 7400,7402, 7404, 7408,7432,7486.
b) Design and fabrication and realization of all gates using NAND/NOR gates.
17. Verification of truth table of Multiplexer(74150)/Demultiplexer(74154)
18. Design and verification of truth tables of half-adder, full-adder and subtractor circuits using gates 7483 and 7486(controlled inverter).
19. To study the operation of Arithmetic Logic Unit IC 74181.
20. Design fabrication and testing of
 - a) Monostable multivibrator of $t = 0.1\text{ms}$ approx. using 74121/123.testing for both positive and negative edge triggering, variation in pulse width and retriggering.
 - b) Free running mutivibrator at 1KHz and 1Hz using 555 with 50% duty cycle. Verify the timing from theoretical calculations.
21. Design and test S-R flip-flop using NOR/NAND gates.
22. Design, fabricate and test a switch debouncer using 7400.
23. Verify the truth table of a JK flip flop using IC 7476,
24. Verify the truth table of a D flip flop using IC 7474 and study its operation in the toggle and asynchronous mode.
25. Operate the counters 7490, 7493 and 74193(Up/Down counting mode). Verify the frequency division at each stage. Using a frequency clock (say 1 Hz) display the count of LED's.
26. Verify the truth table of decoder driver7447/7448. Hence operate a 7 segment LED display through a counter using a low frequency clock. Repeat the above with the BCD to Decimal decoder 7442.

BTEE-501 SYNCHRONOUS MACHINES

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

GENERAL ASPECTS: Construction and working principle of synchronous machines, Excitation systems, production of sinusoidal electromotive force (EMF), flux and magnetomotive force (MMF) phasors in syn. machines; cylindrical and salient pole rotors.

WINDINGS: Classification of windings, pitch factor, distribution factor. Electromagnetic Force equation.

ALTERNATORS: Construction, Phasor diagram of cylindrical rotor alternator, ratings, nature of armature reaction, determination of synchronous reactance; open-circuit characteristics, short-circuit characteristics, short-circuit ratio, short-circuit loss. Effect of variation of power factor on voltage. Determination of voltage regulation: EMF method, MMF. method. Zero power factor (Z.P.F).method. Alternator on infinite bus bar, operation at constant load and variable excitation, power flow through inductive impedance. Power-angle characteristics of synchronous machines:- cylindrical and salient pole. Two reaction theory of salient pole machines, power factor control.

SYNCHRONOUS MOTORS: Operating characteristics, power-angle characteristics, conditions for maximum power developed. V-curves and inverted V-curves, methods of starting, synchronous motors applications, synchronous condensers. Hunting and damper windings.

PARALLEL OPERATION OF ALTERNATORS: Conditions for proper synchronizing for single phase and three phase alternators, conditions for parallel operation, synchronizing power, current and torque, effect of increasing excitation of one of the alternators, effect of change of speed of one of the alternators, effect of unequal voltages, load sharing.

TRANSIENTS: Transient reactances and time constants from equivalent circuits, synchronous machine reactances and their determination, Short circuit. Oscillogram, Synchronization with the grid system, Qualitative introduction to the transient stability of the synchronous machines.

SINGLE PHASE SYNCHRONOUS MOTORS: Reluctance and Hysteresis motors.

BOOKS RECOMMENDED:

6. Bimbhra P.S., *Electrical Machinery*, Khanna Publishers
7. Fitzgerald A.E., Kingsley C. and Umans S.D., *Electric Machinery*, 6th Edition, McGraw Hill
8. Langsdorff E.H., *Principles of D.C. machines*, McGraw Hill
9. Nagrath I.J. and Kothari D.P., *Electrical Machines*, 4th Edition, Tata McGraw Hill,
10. Say M G, *Alternating Current Machines*, 5th edition, Sir Isaac Pitman and Sons Ltd.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-501 PROCESS DYNAMICS AND CONTROL

Internal Marks:	40	L	T	P
External Marks:	60	4	1	0
Total Marks:	100			

Basic Considerations: Introduction, Basic components, diagrammatic representation, symbol and Terminology, changes at arbitrary points in the loop, offset and its analysis.

Process Characteristics: Process variables, mathematical modeling of liquid, gas, and thermal, mechanical and chemical systems, Linearizing techniques, Liquid level control in a tank, Dynamics of manometer, response of non-interacting and interacting first-order elements in series, Mixing process, Heat transfer process, Distillation column.

Controller Characteristics: Control modes, characteristics and comparison of on-off, proportional, integral, derivative modes and their combinations: Proportional-Integral (PI), Proportional-Derivative (PD) and Proportional-Integral-Derivative (PID), Introduction to Digital controllers.

Introduction to Computerized Process Controls: Control algorithm, Proportional-Integral-Derivative (PID) Control action with Dead time.

Automatic Control: Single and combined modes in closed loop, static error, velocity error. Dynamic behaviour of feedback control processes for different modes, IAE, ISE, IATE criteria, Tuning of controllers, process reaction curve.

Controller Hardware: Electronic pneumatic and hydraulic controller's implementation, single and composite modes of controllers.

Final Control Elements: Control valves, types, functions, Electrical, Pneumatic hydraulic-actuators, Solenoid, E-P converters, stepper motors.

BOOKS RECOMMENDED:

1. Johnson Curtin, *Process Control Instrumentation Technology*, Prentice Hall (India)
2. D. Patranabis, *Principles of Process Control*, Tata McGraw Hill
3. G. Stephanopoulos, *Chemical Process Control: An Introduction to Theory and Practice*, Prentice Hall (India)
4. R Coughanowr Donald, *Process Systems Analysis & Control*, Tata McGraw Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-503 MICROPROCESSORS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

Introduction to Microprocessors: Types of computers, Microprocessor evolution and types, Central Processing Unit (CPU) operation and terminology, idea of 8-bit, 16-bit, 32-bit and 64-bit Microprocessors from Intel, Motorola and Zilog and their comparisons.

Introduction to 8-bit Microprocessor: 8085 Microprocessor architecture, classification of instructions, Instruction format, and overview of the 8085 instruction set.

Introduction to 16-bit Microprocessor: 8086 Internal Architecture, Addressing modes, program development steps, 8086 instruction set, Assembler directives, Assembly language, program development tools.

Programming of 8086: Simple sequence programs, jumps, flags, conditional Jumps, IF-THEN, IF-THEN-ELSE, Multiple IF-THEN-ELSE, WHILE-DO, REPEAT-UNTIL, Instruction Timing and delay loops, strings, procedures, Macros.

8086 System Connections, Timing, Troubleshooting: Pin-diagram, maximum/minimum. modes, timing diagrams, use of logic analyzer to observe Bus signals, troubleshooting a simple 8086 based system

8086 Interrupts and Applications: 8086 Interrupts, responses and applications, 8254 software-programmable timer/counter, 8259 a priority Interrupt Controller

Interfacing of 8086: Programmable parallel ports and handshake, Interfacing a Microprocessor to Keyboards and alphanumeric displays, Digital to Analog (D/A) converter operation, interfacing and applications, Analog-to Digital (A/D) converter specifications and Interfacing.

BOOKS RECOMMENDED:

1. Gaonkar, Ramesh S. *Microprocessor Architecture, Programming and Applications with the 8085*, Penram International
2. Ram B, *Fundamentals of Microprocessors and Microcomputers*, Dhanpat Rai and Sons,
3. Hall, Douglas V. *Microprocessors and interfacing: Programming and Hardware*, Tata McGraw Hill
4. Brey, Barry B. *Intel Microprocessors 8086/88, 80186, 286, 386, 486, Pentium Pro Processors, Architecture, Programming and Interfacing*, 4th Edition, Prentice Hall (India)
5. Ray A.K. and Bhurchandi K.M., *Advanced Microprocessors and Peripherals*, Tata McGraw Hill.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-504 POWER ELECTRONICS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

Thyristors and their characteristics: Introduction to Thyristor family, V-I characteristics of silicon-controlled rectifier (SCR), gate turn-off thyristor (GTO), Bidirectional diode for alternating current (DIAC) and Bidirectional, Triode for Alternating Current (TRIAC). Principle of operation of silicon-controlled rectifier (SCR). Two transistor analogy. Turn on methods of a thyristor Switching characteristics of thyristors during turn-on and turn-off. Gate characteristics. Firing of thyristors. Gate triggering circuits. Series and parallel operation of silicon-controlled rectifiers (SCR) and their triggering circuits. Thyristor specifications; such as latching current and holding current, critical rate of rise of off-state voltage (dv/dt) and critical rate of rise of on-state current (di/dt) etc. Protection of SCR from over voltage and over current. Snubber circuits. Power dissipation.

Thyristor commutation techniques: Self commutation by resonating the load (Class A), Self commutation by LC circuit (class B), Complementary commutation (class C), Auxiliary commutation (class D), External pulse commutation (class E), AC Line commutation (class F).

Phase controlled techniques: Introduction to phase angle control. Single phase half wave controlled rectifiers. Single phase half controlled and full controlled bridge rectifiers. Three phase full controlled bridge rectifiers. Effect of resistive, inductive and resistive cum inductive loads. Basic circuit and principle of operation of Dual Converter, circulating current mode and non-circulating current mode of operation. Applications of rectifiers and dual converters to speed control of DC motor drives.

Choppers: Introduction of chopper, Basic chopper classification, Basic chopper operations. Control strategies, Chopper configuration, voltage commutated chopper, Current commutated chopper, Load commutated chopper.

Cycloconverters: Basic principle of operation, Single phase to. single phase cycloconverter. Three phase half wave cycloconverter. Advantages disadvantages of cycloconverters.

Inverters: Introduction & Classification of inverter. Operating principle, Single phase half bridge voltage source inverters, Single phase full bridge inverter. Modified McMurray half-bridge and full-bridge inverter. Three-phase bridge inverter. Voltage control (Pulse-width modulation (PWM) control etc.) and reduction of harmonics in the inverter output voltage. Series inverter.

Symbols and V-I characteristics of Silicon Unilateral Switch (SUS), Silicon Controlled Switch (SCS), Silicon Bilateral Switch (SBS), Unijunction Transistor (UJT), Programmable Unijunction Transistor (PUT), Light-activated silicon-controlled rectifier (LASCR), Reverse conducting Thyristors (RCT), Static Induction Thyristor (SITH), N- Metal Oxide Semiconductor Controlled Thyristor (N-MCT), Field Controlled Thyristors (FCT).

BOOKS RECOMMENDED:

1. Bimbhra, P.S., *Power Electronics*, Khanna Publishers.
2. Singh M.D. and Khanchandani K.B., *Power Electronics*, Tata Mc Graw Hill Publishing company limited.
3. Rashid M.H., *Power Electronics*, Circuits Devices and Applications, Prentice Hall (India)
4. Sen, P.C., *Power Electronics*, Tata McGraw Hill Publishing Company limited.
5. Bhattacharya S.K. and Chatterji, S. *Industrial Electronics and Control*, by New Age international Publications(P) Ltd, New Delhi.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-505 NUMERICAL AND STATISTICAL METHODS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

Floating-Point Numbers: Floating-point representation, Rounding, Chopping, Error analysis, Condition and instability.

Non-Linear Equations: Bisection, Fixed-point iteration and Newton-Raphson methods, Order of convergence.

Linear Systems and Eigen-Values: Gauss-elimination method (using Pivoting strategies) and Gauss-Seidel Iteration method. Rayleigh's power method for Eigen-values and Eigen-vectors

Interpolation: Lagrange's formula with error, divided difference, Newton's divided difference formula

Numerical Integration: Newton-Cote's quadrature formula (with error) and Gauss-Legendre quadrature formula.

Differential Equations: Solution of initial value problem using Taylor Series, Euler's and Runge-Kutta (up to fourth order) methods Statistical Methods

Random Variables: Definition, Probability distribution, Distribution functions, probability distribution function (pdf) and cumulative distribution function (cdf), Expectation and Variance.

Special Probability Distributions: Binomial, Poisson, Geometric, Uniform, Normal and Exponential distributions.

Sampling Distributions: Population and samples, Concept of sampling distributions, Sampling distribution of mean, Chi-square, t and F distributions (pdf only). Tests of Hypotheses: Basic ideas, Important tests based on normal, Chi-square, t and F distribution.

Curve Fitting: Method of least squares, Fitting of simple curves using this method, Regression and Correlation: (Two variables case only)

BOOKS RECOMMENDED:

1. Jain M.K., Iyengar, S.R.K., and Jain R.K., *Numerical Methods for Scientific and Engineering Computation*, New Age International (2008) 5th ed.
2. Conte, S.D and Carl D. Boor, *Elementary Numerical Analysis: An Algorithmic approach*, Tata McGraw Hill, New York (2005).
3. Johnson, R., Miller, I. and Freund, J., *Probability and Statistics for Engineers*, Pearson Education (2005) 7th ed.
4. Gerald C.F and Wheatley P.O., *Applied Numerical Analysis*, Pearson Education (2008) 7th ed.
5. Mathew, J.H., *Numerical Methods for Mathematics, Science and Engineering*, Prentice Hall Inc. (2002).
6. Meyer, P.L., *Introductory Probability and Statistical Applications*, Oxford (1970) 2nd ed.
7. Walpole, Ronald E., Myers, Raymond H., Myers, Sharon L. and, Keying Ye, *Probability and Statistics for Engineers and Scientists*, Pearson Education (2007) 8th ed
8. Sastry S.S., *Introductory Methods of Numerical Analysis*, Prentice Hall (India), (2002), 3rd ed.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-506 Laboratory-VII (Electrical Machines-II)

Internal Marks:	30	L	T	P
External Marks:	20	0	0	2
Total Marks:	50			

Note: Atleast TEN experiments are to be performed in a semester. list of experiments is given below:

List of Experiments:

1. To perform load-test on three-phase Induction motor and to plot torque versus speed characteristics.
2. To perform no-load and blocked-rotor tests on three-phase Induction motor to obtain equivalent circuit. Parameters and to draw circle diagram.
3. To study the speed control of three-phase Induction motor by Kramer's Concept.
4. To study the speed control of three-phase Induction motor by cascading of two induction motors, i.e. by feeding the slip power of one motor into the other motor.
5. To study star- delta starters physically and
 - a) to draw electrical connection diagram
 - b) to start the three-phase Induction motor using it.
 - c) to reverse the direction of three-phase Induction motor
6. To start a three-phase slip –ring induction motor by inserting different levels of resistance in the rotor ckt. And to plot torque –speed characteristics.
7. To perform no-load and blocked-rotor test on single-phase Induction motor and to determine the parameters of equivalent ckt. Drawn on the basis of double revolving field theory.
8. To perform load –test on single-phase. Induction motor and plot torque –speed characteristics.
9. To perform no load and short circuit. Test on three-phase alternator and draw open and short circuit characteristics.
10. To find voltage regulation of an alternator by zero power factor (ZPF.) method.
11. To study effect of variation of field current upon the stator current and power factor with synchronous motor running at no load and draw Voltage and inverted Voltage curves of motor.
12. To measure negative sequence and zero sequence reactance of Synchronous Machines.
13. Parallel operation of three phase alternators using
 - Dark lamp method
 - Two-Bright and one dark lamp method
14. To study synchroscope physically and parallel operation of three-phase alternators using synchroscope.
15. Starting of synchronous motors using
 - Auxiliary motor
 - Using Damper windings

BTEE-507 Laboratory-VIII (Numerical Analysis)

Internal Marks:	30	L	T	P
External Marks:	20	0	0	2
Total Marks:	50			

Note: Atleast TEN experiments are to be performed in a semester. list of experiments is given below:

List of Experiments:

To develop algorithms/programs in C or C++ or FORTRAN-77/90/95 or MatLab language for the following methods

1. Lagrange's formula with error, divided difference for interpolation,
2. Newton's divided difference method for interpolation and extrapolation.
3. Bisection method for finding a real root of an equation.
4. Newton Raphson method for finding a real root of an equation.
5. Iteration method for finding a real root of an equation.
6. Gauss elimination method for solving simultaneous linear algebraic equations.
7. Gauss Jordan method for solving simultaneous linear algebraic equations.
8. Simpson's 1/3rd rule for numerical integration.
9. Newton's forward interpolation formula.
10. Lagrange's method for interpolation.
11. Euler's method for solving ordinary differential equations.
12. Runge-Kutta (up to fourth order) method for solving ordinary differential equations.
13. Curve fitting (linear and polynomial)

BTEE-508 Laboratory-IX (Process Control)

Internal Marks:	30	L	T	P
External Marks:	20	0	0	2
Total Marks:	50			

Note: At least TEN experiments are to be performed in a semester. list of experiments is given below:

List of Experiments:

1. Identify and apply the essential building blocks in automatic control
2. Specify and design the loop requirements for a plant using PID control Plant constitutes a is a hydraulic, pneumatic and electric systems
3. Apply the procedures for open and closed loop tuning (hydraulic, pneumatic, and electric systems).
4. Tune control loops with significant dead-times
5. Demonstrate a clear understanding of analog process control and how to tune analog loops Plant constitutes a is a hydraulic, pneumatic and electric systems

BTEE-601 ELECTRIC POWER UTILIZATION

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Electric Drives: Electrical drives & Mechanical drives, Concept of electrical drives, Basic features of industrial drives, review of operating and starting characteristics of different types of electric motors for various drives (AC and DC motors). Estimation of rating and heating of motors, Load equalization (Fly wheel effect), Drives for particular services.

Electric Traction: Introduction to Indian railways system , Electric Locomotive Classes, Various types of Traction system, single phase feeding arrangement prevalent in India. Substation. arrangements, Different Types of Catenary construction and line insulation, Span and dropper design Calculations.

Electric Heating and Welding: Methods of electric heating, types of electric heating, constructional details and performance of resistance heating furnace. Dielectric heating, Alternating current (AC).and Direct current (DC) Welding, Resistance and Arc Welding. Electric Beam Welding, Laser Welding. Typical construction of electrical welding AC and DC set.

Illumination: Production of light by different methods, terms used, laws of illumination, Different Artificial light sources, their construction and operating principles, Design of lighting schemes and equipment used for indoor, industrial and flood lighting.

Refrigeration and Air conditioning: Refrigeration system, Domestic refrigeration, Air conditioner, Comfort Air conditioning, Effective temperature.

Electrolysis: Laws of Electrolysis, Process voltage, current, energy, efficiency, Applications of electrolysis.

BOOKS RECOMMENDED:

1. Partab H., *Modern Electric Traction*, Dhanpat Rai
2. De N.K. and Sen P.K., *Electric Drives*, PHI publication
3. Berde M.S., *Electric Motor Drives*, Khanna Publishers
4. Gupta J.B., *Utilization of Electric Power and Electric Traction*, S.K. Kataria and Sons
5. Tripathy S. C., *Electric Energy Utilization and Conservation*, Tata McGraw Hill
6. Taylor E.O., *Utilization of Electric Energy*, Orient Blackswan
7. Hughes Austin, *Electric Motors and Drives: Fundamentals, Types and Applications*, Newnes, (2005)

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-602 POWER SYSTEM-II (Switch Gear and Protection)

Internal Marks:	40	L	T	P
External Marks:	60	3	1	0
Total Marks:	100			

Sub-Station: Types, Main equipment in Substation, substation layout, Busbar-arrangements.

Isolators and Fuses: Isolating switches functions, Types, Rating and operation. Fuse-types, Rating, Selection, theory and characteristics, applications.

Circuit Breakers: Need for Circuit Breakers, Arc phenomenon, Theory of Arc Interruption, Recovery Voltage and Restriking Voltage, Various Types of Circuit Breakers. Principles and Constructional Details of Air Blast, Minimum Oil, SF₆, Vacuum Circuit Breakers etc.

Protective Relays: Introduction, classification, constructional features; and Characteristics of Electromagnetic, Induction, Thermal, Overcurrent relays, Directional relays, Distance relays, Differential, Translay, Negative sequence relay, introduction to static and up-based relays.

Protection of Feeders: Time graded protection, Differential and Distance protection of feeders, choice between Impedance, Reactance and Mho relays, Elementary idea about carrier current protection of lines.

Protection of Generators and Transformers: Types of faults on alternator, Stator and rotor protection, Negative sequence protection, Loss of excitation and overload protection. Types of fault on transformers, percentage differential protection, Gas relays.

Protection against over voltage and earthing: Ground wires, Rod gap, Impulse gap, Valve type and Metal Oxide Arresters, Line Arrester/Surge Absorber. Ungrounded neutral system, Grounded neutral system and Selection of Neutral Grounding.

BOOKS RECOMMENDED

1. Rao S., *Switchgear and Protection*, Khanna Publishers
2. Chakrabarti A., Soni, M.L. Gupta P.V. and Bhatnagar U.S., *A Textbook on Power System Engineering*, Dhanpat Rai and Co.
3. Wadhawa C.L. , *A Course in Electrical Power*, New Age international Pvt. Ltd
4. Badri Ram and Vishwakarma D.N., *Power system Protection and Switchgear*, Tata McGraw Hill
5. Deshpande M.V., *Switchgears and Protection*, Tata McGraw Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-603 NON-LINEAR AND DIGITAL CONTROL SYSTEM

Internal Marks:	40	L	T	P
External Marks:	60	4	1	0
Total Marks:	100			

STATE VARIABLE TECHNIQUES: State variable representation of systems by various methods, solution of state variable model. Controllability and observability.

PHASE PLANE ANALYSIS: Singular points, Method of isoclines, delta method, phase portrait of second order nonlinear systems, limit cycle.

DESCRIBING FUNCTION ANALYSIS: Definition, limitations, use of describing function for stability analysis, describing function of ideal relay, relay with hysteresis, dead zone, saturation, coulomb friction and backlash.

LYAPUNOV'S STABILITY METHOD: Lyapunov's direct method, generation of Lyapunov's function by Krasovskii's and Variable Gradient methods

SAMPLED DATA SYSTEMS: Sampling process, mathematical analysis of sampling process, application of Laplace transform. Reconstruction of sampled signal, zero order, first order hold. Z-transform definition, evaluation of Z-transform, inverse Z-transform, pulse transfer function, limitations of Z-transform, State variable formulation of discrete time systems, solution of discrete time state equations. Stability definition, Jury's test of stability, extension of Routh-Hurwitz criterion to discrete time systems.

BOOKS RECOMMENDED:

1. Ogata K., *Modern control engineering*. Prentice Hall (India)
2. Nagrath I.J., Gopal M., *Control system engineering*, New Age Publications
3. Hsu J.C. and Meyer A.U., *Modern control principles and application*
4. Gopal M., *Digital Control and State Variable Methods*, Tata McGraw Hill
5. Kuo B.C. and Golnaraghi F., *Automatic Control System*, Wiley Publications
6. Dorf R.V. and Bishop R.H., *Modern Control Systems*, Addison Wesley

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-604 MICROCONTROLLER AND PLC

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction: Microprocessor, Micro-controllers and their comparison. The 8051 Architecture: Introduction, 8051 micro-controller hardware, input/ output, pins, ports and circuits, external memory, counters and timers, serial data input/ output, interrupts

8051 Assembly Language Programming: The mechanics of programming, assembly language programming process, programming tools and techniques, instruction set (data moving, logical operations, arithmetic operations, jump and call instructions)

8051 Microcontroller Design: Micro-controller specification, external memory and memory space decoding, reset and clock circuits, expanding input and output (I/O), memory mapped I/O, memory address decoding, memory access times, testing the design, timing subroutines, lookup tables for the 8051, serial data transmission

Microcontroller Applications: Interfacing keyboards, displays, Digital-to-Analog (D/A) and Analog-to-Digital (A/D), multiple interrupts, serial data communications, introduction to the use of assemblers and simulators Embedded Systems: Introduction to PLDs and FPGA- architecture, technology and design issues, implementation of 8051 core.

Programmable Logic Controllers (PLC): Introduction, operation of PLC, difference between PLC and Hardwired system, difference between PLC and Computer, relay logic and ladder logic, ladder commands and examples of PLC ladder diagram realization, PLC timers, PLC counters, PLC classification.

RECOMMENDED BOOKS:

1. Kenneth J Ayola, *The 8051 Micro Controller- Architecture, Programming and Application*, Penram International Publication
2. John B Peatman, *Design with Micro Controller*, Tata McGraw Hill
3. Ray A. K. and Bhurchandi K. M., *Advanced Microprocessors and Peripherals; Architecture, Programming and Interfacing*, Tata McGraw Hill
4. Mazidi M. A. and Mazidi J. G., *The 8051 Micro-controller and Embedded System*, Pearson Education.
5. Udayashankara V. and Mallikarjunaswamy M.S., *8051 Microcontroller Hardware, Software and Applications*, TataMcGraw Hill Education Pvt. Ltd., (2010)
6. Surekha Bhanot, *Process Control*, Oxford Higher Education.
7. Otter, Job Dan, *Programmable Logic Controller*, P.H. International, Inc, USA
8. Dunning Gary, *Introduction to PLCs*, Tata McGraw Hill
9. Kumar Rajesh, *Module on PLCs and their Applications*, NITTTR Chandigarh

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-609/BTEIC-805C/BTEE-805C VIRTUAL INSTRUMENTATION

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction to Virtual Instrumentation: Historical perspective, Classification of different instruments / instrumentation system, Definition and architecture of virtual instrumentation system, salient features, and application area of virtual instrumentation.

Data Flow Programming Techniques: Graphical programming in data flow, comparison with conventional programming, popular data flow and VI software packages. Building a VI front panel and block diagram, sub VI, for and while loops, case and sequence structure, formula nodes, local and global, string and file I/O, array and clusters, charts and graphs, attributes nodes.

Data Acquisition Basics: ADC, DAC, D/O, counters and timer, PC hardware structure, timing, interrupts, DMA, software and hardware installation, Configuring data acquisition hardware using the drives in application software, use of DAQ library functions for different analog and digital input/output operations.

Common Instrument Interfaces: Current loop, RS 232, RS485, GBIP. Use of library functions to communicate with different instruments.

Use of Measurement Analysis Tools: Measurement of Max, Min, Peak-Peak voltage, Mathematical tools, time period of a signal, power spectrum and logging Fourier transform, Correlation methods, windowing and filtering.

Building a web based virtual instrument: Networking basics for office and industry application.

BOOKS RECOMMENDED:

1. Gupta, *Virtual Instrumentation Using Labview*, Tata McGraw Hill
2. S Gupta & J Gupta, *PC Inerfacing for data acquisition*, ISA
3. Wells Lisa K, Travis Jeffry, *LabVIEW for everyone*, Prentice Hall (India)
4. Johnson Gary W, *Lab view Graphical Programming*, McGraw Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-610/BTEIC-804D/BTEE-804D INDUSTRIAL PROCESS CONTROL

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Description And Modeling of Various Industrial Processes: Model Classification, Mathematical Models, Physical Models, Analog Models, Estimation of Model Parameters, System Identification, Experimental Nature of Simulation, Steps Involved in Simulation Studies, Validation of Simulation Models, Computer Simulation of Continuous and Discrete Systems, Examples

Process Control: Types and Description of Processes, Blending, batch processes, compressor and chiller controls, distillation control, steam turbine and water treatment controls, boiler controls, reactor controls

Conventional Controllers: On-off Controllers, Cascade and Feed forward Controllers, Split Range Controllers, ratio controls, Single loop, multi loop and self tuning controllers, set point control (SPC), discrete digital control (DDC)

Intelligent Controllers: Fuzzy logic control, programmable logic controllers, PC based system, conventional and Windows-NT based distributed control system (DCS) systems, artificial intelligence and neural networks, smart and intelligent transmitters.

BOOKS RECOMMENDED:

1. Padmanabhan, Tattamangalam R, *Industrial Process Instrumentation and control* Springer Publishing
2. Andrew W.G. and Williams H.B., *Applied Instrumentation in the Process Industries*, Gulf Publishing, Houston
3. Nolting B.E., *Instrumentation Reference Book*, Elsevier India Pvt, New Delhi
4. Liptak B.G., *Instrument Engineer's Handbook (Process Control)*, Elsevier India Pvt, New Delhi.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-605A/BTEE-403 ELECTROMAGNETIC FIELDS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

REVIEW OF VECTOR ANALYSIS: Vector analysis, Physical interpretation of gradient, divergence and curl; vector relations in other coordinate systems, integral theorems: divergence theorem, stoke's theorem, green's theorem and Helmholtz theorem.

ELECTROSTATICS: Introduction to fundamental relations of electrostatic field; Gauss's law and its applications; potential function; Field due to continuous distribution of charges; Equipotential surfaces; Divergence theorem; Poisson's equation and Laplace's equation, capacitance, electrostatic energy, Conditions at Boundary between dielectrics, Uniqueness theorem.

STEADY MAGNETIC FIELD: Magnetic induction and Faraday's laws; magnetic Flux Density; magnetic field strength and magnetomotive force; Ampere's work Law in the differential vector form; permeability; energy stored in a magnetic field ; ampere's force law; magnetic vector potential, Analogies between electric and magnetic fields.

MAXWELL'S EQUATIONS AND POYNTING VECTOR: Equation of continuity for time varying fields, Inconsistency of ampere's law, Maxwell's equations in integral and differential form for static and time varying fields, conditions at a Boundary surface, Concept of Poynting vector, Poynting Theorem, Interpretation of $\mathbf{E} \times \mathbf{H}$

ELECTROMAGNETIC WAVES: Solutions for free-space conditions; Uniform plane Wave Propagation; Wave equations for a conducting medium; Sinusoidal time variations; Polarization; Conductors and Dielectrics; Direction Cosines; Reflection by Perfect Conductor -normal and oblique incidence, Perfect Dielectric-normal incidence, Perfect Insulator –Oblique incidence; Brewster angle, Reflection at a surface of Conductive medium, Surface impedance.

BOOKS RECOMMENDED

1. Edward C. Jordan and Keith G Balmain, *Electromagnetic Waves and Radiating Systems*, Prentice-Hall Inc.
2. Kraus John D. *Electromagnetics*, McGraw-Hill Publishers
3. Edminister Joseph A., *Schaum's Theory and Problems of Electromagnetics*, McGraw-Hill
4. Rao N. Narayana, *Elements of Engineering Electromagnetics*, Pearson Education

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-605B/BTEE-502 ELECTRIC GENERATION AND ECONOMICS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

Introduction: Electrical energy sources, organization of power sector in India, single line diagram of thermal, hydro and nuclear power stations. Classification of power plants in base load and peak load plants

Loads and Load curves: Types of load (fixed voltage loads, resistive loads, Inductive motor loads, Mechanical load), effect of load on supply voltage, Maximum demand, Group diversity factor, Peak diversity factor, Types of load, chronological load curves, load-duration Curve, mass curves, load factor, capacity factor, utilization factor, base load and peak load plants, load forecasting.

Power Plant Economics: Capital cost of plants, annual fixed cost, operating costs and effect of load factor on cost of energy, depreciation.

Tariffs and power factor improvement: Objectives of tariff making, different types of tariff (domestic, commercial, agricultural and industrial loads). Need for power factor (p.f.) improvement, power factor improvement using capacitors, determination of economic power factor.

Selection of plant: Plant location, plant size, number and size of units in plants, economic comparison of alternatives based on annual cost, rate of return, present worth and capitalized cost methods.

Economic operation of steam plants: Methods of loading turbo-generators, input- output curve, heat rate, incremental cost, method of Lagrangian multiplier, effect of transmission losses, co-ordination equations, and iterative procedure to solve co-ordination equations.

Hydro-thermal co-ordination: Advantages of combined working of Run-off River plant and steam plant, reservoir hydro plants and thermal plants, long-term operational aspects, scheduling methods.

Pollution and environmental problems: Energy and environment, Air pollution, Aquatic impacts, nuclear plant and hydro plant impacts.

Cogeneration: Definition and scope, Topping and Bottoming Cycles, Benefits, cogeneration technologies.

BOOKS RECOMMENDED:

1. Deshpande M.V., *Power Plant Engineering*, Tata McGraw Hill (2004).
2. EI-Wakit M.M., *Power Plant Engineering*, McGraw Hill, USA
3. Rajput R.K., *Power Plant Engineering*, Luxmi Publications
4. Sharma P.C., *Power Plant Engineering*, Kataria and Sons
5. Skrotzki B.G.A. and Vapot W.A., *Power Station Engineering and Economy*, Tata McGraw-Hill
6. Arora S.C. and Dom Kundwar S., *A course in Power Plant Engineering*, Dhanpat Rai.
7. Nag, P.K., *Power Plant Engineering*, Tata McGraw Hill
8. Gupta B.R., *Generation of Electrical Energy*, S. Chand (1998).
9. Nagrath I.J. and Kothari D.P., *Power System Analysis* Tata McGraw-Hill Publication
10. Chakrabarti A., Soni, M.L. Gupta P.V. and Bhatanagar U.S., *A Textbook on Power System Engineering*, Dhanpat Rai and Co.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-605C/BTEE-605C INSTRUMENTATION IN POWER SYSTEM

Internal Marks:	40	L	T	P
External Marks:	60	3	1	0
Total Marks:	100			

Introduction: Measurement of electrical quantities, Active and reactive power in power plants, Energy meters, Instrument transformers and their transient response.

Instrumentation Techniques: Telemetry, Remote Control, remote signaling and supervisory control and data acquisition (SCADA), signal formation, conversion and transmission.

Signal Transmission Techniques: Analog pulse and digital modulation, Amplitude modulation(AM) and Frequency modulation (FM), AM and FM Transmitter and Receiver, Phase Modulation, Pulse modulation, Digital transmission techniques, error detection and correction.

Telemetry: Telemetry errors, DC, pulse and digital telemetry methods and systems.

Supervisory Control and Data Acquisition: Function of SCADA system remote terminal unit (RTU) details, Control center details, Communication between control centers, control center and remote terminal unit.

Power Plant Instrumentation: Hydroelectric power plant instrumentation, Thermal power plant instrumentation, Nuclear Power plant Instrumentation. Applications of SCADA system to Indian Power Systems.

RECOMMENDED BOOKS:

1. Cegrell,T., *Power System Control Technology*, Prentice-Hall of India Private Limited(2001).`
2. Lindsley, D.M. , *Power Plant Control and Instrumentation*, IEEE Press (2000).
3. Jarvis, E.W., *Modern Power Station Practice: Control and Instrumentation (Vol. F)*, British Electricity International (1980).

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-605D/BTEE-605D BIOMEDICAL INSTRUMENTATION

Internal Marks:	40	L	T	P
External Marks:	60	3	1	0
Total Marks:	100			

Transducers: Strain gauge for respiratory flow transducer, piezo resistive transducer for intracardiac catheter, thermistor as temperature sensing elements - its characteristics and compensation for non-linearity.

Piezoelectric transducer: its equivalent circuits and impedance frequency characteristics. Its applications as intra cardiac microphone, heart assist device and ultrasonic instruments. Variable inductance transducer, different configuration and application for measurement of muscular tremor. linear variable differential transformer (LVDT) and its signal processing circuitry. Magnetostrictive and variable capacitance transducers, stretched diaphragm transducer and its characteristics.

Measurement and recording of bioelectric signals: electrocardiogram (ECG), electromyogram (EMG), electroencephalogram (EEG) and instruments for picking up and reproducing bioelectric signals, specific design characteristics, sources of noise and its removal.

Measurement and recording of non-electric signal: Measurement and recording of pressure, temperature, respiration rate, pulse rate and blood flow. Electromagnetic blood flow meter, thermography, pH measurements, gas analysis, ESR (erythrocyte sedimentation rate) measurement, plethysmograph, X-Ray, tonometer and dialysis. Ultrasonics and echoencephalography radiography imaging isotopes and nuclear medicine.

Equipment for effecting the human body: Stimulator, defibrillator, pacemaker, diathermy.

Prosthetics: Upper and lower extremity prostheses, harness control, EMG-controlled externally powered prosthesis, basic concept of monofunctional and multifunctional devices.

Biotelemetry: Radio-telemetry of biological signal, signal source, antenna and frequency design considerations, example of single channel FM units.

RECOMMENDED BOOKS:

1. Walter Welkowitz and Sid Deutch, *Biomedical Instruments, theory and design*, Academic press 1976.
2. Guha S.K., *An Introduction to Medical Electronics*, Bharti Publishers, Patna.
3. Harry E. Thomas, *Handbook of Biomedical Instrumentation and Measurement*, Reston Publishing Company, 1974.
4. Marvin D. Weisis, *Biomedical Instrumentation*, Chilton Book Company, 1973.
5. Geddes L.A., Barker L.E., *Principles of Applied Medical Instrumentation*, John Willey and Sons, 1968.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-605E/BTEE-605E PRINCIPLES OF COMMUNICATION SYSTEMS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Review of Communication Engineering: Introduction, amplitude modulation (AM), Frequency modulation (FM), their side bands, comparison, sampling theorem, different pulse modulation techniques- Pulse-amplitude modulation (PAM), Pulse width modulation (PWM), Pulse-position modulation (PPM) and Pulse-code modulation (PCM), frequency-division multiplexing (FDM), Time division multiplexing (TDM). Introduction to Fourier series and Fourier transform of periodic signals. Transfer functions and properties of practically realizable filters.

Radio Transmitters: Block diagram explanation of low and high level amplitude modulation (AM) transmitter, amplitude modulation broadcast transmitter, double sideband (DSB) transmitter, single sideband (SSB) transmitter and Independent sideband transmitter, block diagram and explanation of reactance tube and Armstrong Frequency modulation (FM) transmitters, Stereophonic FM broadcast transmitter.

Radio Receiver: Amplitude modulation diode detector, characteristics of radio receiver: sensitivity, selectivity, fidelity and image rejections, classification of radio receivers, Tuned radio frequency (TRF) receiver and super heterodyne receiver, block diagram explanation of amplitude modulation receiver, amplitude modulation receiver using Phase lock loop (PLL), double sideband and single sideband receiver, Independent sideband receiver, amplitude modulation broadcast receiver, noise in amplitude modulation systems, Frequency modulation detection, block diagram explanation of Frequency modulation receiver and stereophonic Frequency modulation broadcast receiver, noise in Frequency modulation systems.

Television Engineering: Principle of camera, introduction of picture tube, scanning, frame, field, sync video signal, vestigial sideband transmission, block diagram of Television (TV) receiver and working, Television (TV) transmitter.

RECOMMENDED BOOKS:

1. Mittal G. K., *Radio and TV Engineering*, Khanna Publishers
2. Kennedy, *Electronic communication system*, McGraw Hill
3. Gulati R. R., *Monochrome and colour TV*, Dhanpat Rai and Sons.
4. Taub and Schilling, *Principles of communications*, McGraw Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-605F/BTEE-605F MICROELECTRONICS TECHNOLOGY

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction: Classification, Scale thick film, Thin film and hybrid integrated circuits, hybrid Integrated Circuit (IC) Fabrication and component design, resistor, capacitor, and inductor, Design and fabrication.

Monolithic Architecture: Process on silicon crystal, line growth, refining substrate slicing, Polishing, chemical vapour deposition, thermal oxidation, photolithography, diffusion, dopant impurities, diffusion system, ion implantation, metallization, isolation.

Monolithic Components: Bipolar IC process, Metal Oxide Silicon (MOS) IC process, Bipolar Junction Transistor (BJT) construction, diode instruction, Field Effect Transistor (FET) and MOS construction and resistor, capacitor, and inductor, op –amp and voltage regulator, design and fabrication.

Large Scale Integrated (LSI) Circuits: Brief introduction to LSI circuits, realizations of inter connection and realization of integrated elements.

Basic Very Large Scale Integrated (VLSI) Design: Basic MOS transistor and working, n-channel MOS and Complementary MOS (CMOS) fabrication, thermal aspects MOS and VLSI, some electric properties of MOS, design process, circuit component, system design and layout and scaling of MOS circuit, some application of VLSI circuit like Programmable Logic Array (PLA).

RECOMMENDED BOOKS:

1. Botkar K. R., *Integrated Circuits Interfacing Techniques in Digital design*, Khanna Publishers
2. Sze, *VLSI Design*, TMH
3. Gray Paul R., *Analog MOS IC Circuit Design*, IEEE Press.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEE-606 Laboratory-X (Power Electronics and Drives)

Internal Marks: 30
External Marks: 20
Total Marks: 50

L	T	P
0	0	2

Note: Atleast TEN experiments are to be performed in a semester. list of experiments is given below:

List of Experiments:

1. To study principle of operation of SCR, plot V-I characteristics and study the effect of gate triggering on turning on of SCR.
2. To draw V-I characteristics of an UJT and to use UJT as relaxation oscillator.
3. To study the effect of free-wheeling diode on power factor for single phase half-wave rectifier with R-L load.
4. To plot waveforms for output voltage and current, for single phase full-wave, fully controlled bridge rectifier, for resistive and resistive cum inductive loads.
5. Study of the microprocessor based firing control of a bridge converter.
6. To study three phase fully controlled bridge converter and plot waveforms of output voltage, for different firing angles.
7. Study of Jones chopper or any chopper circuit to check the performance.
8. Thyristorised speed control of a D.C. Motor.
9. Speed Control of induction motor using thyristors.
10. Study of series inverter circuit and to check its performance.
11. Study of a single-phase cycloconverter.
12. To check the performance of a McMurray half-bridge inverter

BTEE-607 Laboratory-XI (Power System-II)

Internal Marks: 30
External Marks: 20
Total Marks: 50

L	T	P
0	0	2

Note: Atleast TEN experiments are to be performed in a semester. list of experiments is given below:

List of Experiments:

1. To study the performance of a transmission line. Also compute its ABCD parameters.
2. Study of Characteristics of over current and earth fault protection.
3. To study the operating characteristics of fuse. (HRC or open type)
4. To find the earth resistance using three spikes
5. To study over current static relay.
6. To study the different types of faults on transmission line demonstration panel/model.
7. To study the radial feeder performance when
 - a. Fed at one end
 - b. Fed at both ends
8. To study the performance of under voltage and over voltage relay.
9. To study the characteristics of bimetal mini circuit breakers.
10. To study the characteristics of Distance Relay.
11. To find the breakdown strength of transformer oil.

BTEE-608 Laboratory-XII (Micro controller and PLC)

Internal Marks: 30
External Marks: 20
Total Marks: 50

L	T	P
0	0	2

Note: Atleast TEN experiments are to be performed in a semester. list of experiments is given below:

List of Experiments:

1. Study of 8051/8031 Micro-controller kits.
2. Write a program to add two numbers lying at two memory locations and display the result.
3. Write a program for multiplication of two numbers lying at memory location and display the result.
4. Write a program to check a number for being ODD or EVEN and show the result on display.
5. Write a program to split a byte in two nibbles and show the two nibbles on display.
6. Write a program to arrange TEN numbers stored in memory location in ascending and descending order.
7. Write a program to find a factorial of a given number.
8. Study of interrupt structure of 8051/8031 micro-controllers.
9. Write a program to show the use of INT0 and INT1.
10. Write a program of flashing LED connected to port 1 of the micro-controller.
11. Write a program to control a stepper motor in direction, speed and number of steps.
12. Write a program to control the speed of DC motor.
13. Implementation of different gates using PLC.
14. Implementation of DOL and star delta starter using PLC.
15. Implement basic logic operations, motor start and stop operation using
 - (i) Timers
 - (ii) Counters
16. Motor forward and reverse direction control using PLC.
17. Write and implement the LD control program for rack feeder.
18. Make a PLC based system for separating and fetching work pieces.
19. Make a PLC based control system for conveyor belt.
20. Implement a PLC based traffic light control.

BTEIC-801 ADVANCED PROCESS CONTROL

Internal Marks:	40	L	T	P
External Marks:	60	4	1	0
Total Marks:	100			

Introduction: Review and limitations of single-loop control, need for multi-loop systems.

Advanced Process Control Techniques: Concept of multiloop process controls, analysis and applications of cascade, ratio, Feed forward, override, split-range, selective and Auctioneering Control Systems with multiple loops, Dead time compensation, Adaptive control, inferential control.

Design of control systems for multivariable process: Multivariable control system, interaction in multiple loops, RGA method for minimizing interactions e.g. distillation column, absorbers, Heat Exchangers, Furnaces and Reactors, P-I diagrams, standard instrumentation symbols for devices, signal types, representation and reading of instrumentation scheme using PI diagrams.

Introduction to Computer Control systems in Process Control: DCS Configuration, control console equipment, communication between components, local control units, DCS flow sheet symbols, DCS I/O hardware and setpoint stations, Supervisory control and data acquisition system (SCADA).

Programmable Logic Control: Introduction, relative merits over DCS and relays, programming languages, Hardware and system sizing, PLC installation, Maintenance and trouble shooting.

BOOKS RECOMMENDED:

1. Johnson Curtin, *Process Control Instrumentation Technology*, Prentice Hall (India)
2. G. Stephanopoulos, *Chemical Process Control: An Introduction to Theory and Practice*, Prentice Hall (India)
3. Coughanowr R. Donald, *Process Systems Analysis & Control*, Tata McGraw Hill
4. B G Liptak, *Handbook of Process Control*, Chilton Press

BTEIC-802 DATA ACQUISITION AND TELEMETRY

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

Introduction: Generalized data transmission systems, analog & digital data Transmission and their comparison, block diagram of data acquisition System & its applications, introduction to electrical telemetry, its needs & block diagram.

Data Acquisition Systems: Analog & digital acquisition systems, signal conditioning of the inputs ratio metric conversion & logarithm compression, single channel data acquisition, multi-channel DAS, computer based DAS, digital to analog converters, analog to digital converters, electromechanical ADC, uses of data acquisition systems, use of recorders in digital systems & block diagram of digital data recording system, data logging system, compact data logger , modem digital data acquisition, sensors based computer data system, digital transducer.

Data Conversion & Transmission: Methods of data transmission, transmission channels & media, Modulation & demodulation, amplitude, frequency & phase modulation, Comparison between frequency & amplitude, pulse modulation (PAM, PDM, PFM, POM), delta modulation, adaptive data modulation & Companding, digital data codes, error correcting & error detecting codes, Asynchronous & synchronous data transmission, pulse code formats used in data transmission, radio link, frequency division & time division multiplexing, time division multiplexing using mechanical commutator, electronic time division multiplexing system, block diagram of AM frequency division multiplexing system.

Telemetry & Remote Control: Various classes of telemetry systems, land-line & radio telemetry, dc Telemetry , voltage current & position telemetry, land-line telemetry feedback systems, ac telemetry, frequency modulation telemetry systems, complete telemetry package including telemetry transmitting & receiving systems, case studies of remote control & telemetry applied to communication based processing control systems (pipeline control & power system control) & biomedical engineering.

BOOKS RECOMMENDED:

1. D. Patranabis, *Telemetry Principles*, Tata McGraw Hill
2. A.K. Sawhney, *A Course in Electrical & Electronics Measurement & Instrumentation*, Dhanpat Rai
3. Munez-Ruiz, Angel; Vromans, Herman, *Data Acquisition and Measurement Techniques*, CRC Press
4. Elliot L. Gruenberg, *Handbook of Telemetry & Remote Control*, Tata McGraw Hill
5. H S Kalsi, *Electronic Instrumentation*, Tata McGraw Hill

BTEE-803 NON-CONVENTIONAL ENERGY SOURCES

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

INTRODUCTION: Limitation of conventional energy sources, need and growth of alternative energy source, basic scheme and application of direct energy conservation.

MHD GENERATORS: Basic principles, gaseous, conduction and hall effect, generator and motor effect, different types of Magneto-Hydro-Dynamic (MHD) generator, types of MHD material, conversion effectiveness, analysis of constant area MHD generator, practical MHD generator, application and economic aspects.

THERMO-ELECTRIC GENERATORS: Thermoelectric effects, Seebeck effect, Peltier effect, Thomson effect, thermoelectric converters, figures of merit, properties of thermoelectric material, brief description of the construction of thermoelectric generators, application and economic aspect.

PHOTOVOLTAIC EFFECT AND SOLAR ENERGY: Photovoltaic effect, different types of photovoltaic cells, cell fabrication, characteristics of photovoltaic cells, conversion efficiency, solar batteries, application, solar radiation analysis, solar energy in India, solar collectors, solar furnaces and applications.

FUEL CELLS: Principle of action, Gibb's free energy, general description of fuel cells, types, construction, operational characteristics and application.

MISCELLANEOUS SOURCES: Geothermal system, hydro-electric plants, wind power, tidal energy, Bio-mass energy

BOOKS RECOMMENDED:

1. Gupta B. R., *Generation of Electrical Energy*, S. Chand.
2. Rai, G.D., *Non Conventional Energy Sources*, Khanna Publishers (2005).
3. Rao, S. and Parulekar, B.B., *Energy Technology: Non Conventional, Renewable and Conventional*, Khanna Publishers (2005).
4. Wadhwa, C.L., *Generation, Distribution and Utilization of Electric Energy*, New Age International (P) Limited, Publishers (2007).
5. Simon, Christopher A., *Alternate Source of Energy*, Rowman and Littlefield Publishers Inc.(2007).
6. Venikov, V.A. and Putyain, E.V., *Introduction to Energy Technology*, Mir Publishers (1990).
7. Chakrabarti A., Soni M. L., Gupta P. V. and Bhatnagar U. S., *Power System Engineering*, Dhanpat Rai and Co.
8. Kothari D.P., Singal K.C. and Ranjan R., *Renewable Energy Sources and Emerging Technologies*, Prentice Hall (India)

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-804A/BTEE-801 POWER SYSTEM ANALYSIS

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

SYSTEM MODELLING: System modelling of synchronous machines, transformers, loads etc, per unit system, single line diagram of electrical networks, single phase impedance diagrams. Formulation of impedance and admittance matrices for the electrical networks.

LOAD FLOW STUDIES: Data for the load flow studies, Swing Bus, Formulation of simultaneous equations, Iterative solutions by the Gauss-Seidal method and Newton Raphson Method.

FAULT ANALYSIS: Transients on transmission line, short circuit of synchronous machine, selection of circuit breakers, Algorithm for short circuit studies, Symmetrical Component transformation, construction of sequence networks of power systems. Symmetrical Analysis of Unsymmetrical Line-to-ground (LG), Line-to line (LL), double line to ground (LLG) faults using symmetrical components.

POWER SYSTEM STABILITY: Steady state stability, Dynamics of a synchronous machine , Power angle equations , Transient stability, equal area criterion, Numerical solution of swing equation , factors effecting transient stability.

BOOKS RECOMMENDED:

1. Elgerd O.I., *Electric Energy Systems Theory*, Tata McGraw Hill
2. Nagrath I.J., Kolthari D.P., *Modern Power System Analysis*, Tata McGraw Hill
3. Stevenson W.D., *Elements of Power System Analysis*, McGraw Hill
4. Nagrath I.J. and Kothari D.P., *Power System Engineering*, Tata McGraw Hill
5. Arrillaga J. and Arnold C.P., *Computer Analysis of Power Systems*, John Wiley & Sons
6. Stagg Glenn W. and Ei-Abiad Ahmed H., *Computer Methods in Power System Analysis*, Tata McGraw Hill
7. Kusic G.L., *Computer Aided Power System analysis*, Prentice Hall, India

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-804B/BTEE-802 HIGH VOLTAGE ENGINEERING

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
4	1	0

Extra High Voltage (EHV) Transmission and Corona Loss: Need for EHV Transmission. Use of bundled conductors, corona characteristics of smooth bundled conductors with different configurations, Corona loss. Factors affecting the corona loss. Radio interference due to corona. Shunt and series compensation in EHV lines. Tuned power lines. Insulation Co-ordination.

High Voltage Direct Current (HVDC) Transmission: Advantages, disadvantages and economics of HVDC Transmission system. Types of Direct Current (DC) links, converter station equipment, their characteristics.

Insulating materials for High Voltage Applications of insulating materials used in power transformers rotating machines, circuit breakers, cables, power capacitors.

Conduction and breakdown in Gases, Liquids and Solid Dielectrics:

Solids - Intrinsic, electromechanical and thermal breakdown composite dielectrics, solid dielectrics used in practice.

Liquids:- Conduction and breakdown in pure and commercial liquids, suspended particle theory, cavitation and bubble theory, stressed oil volume theory, Liquids used in practice.

Gases:- Ionization process, Townsend's current growth equations, 1st and 2nd ionization coefficients. Townsend's criterion for breakdown. Streamer theory of breakdown, Paschen's law of Gases. Gases used in practice.

Generation of High Voltages: High Voltage Direct Current (HVDC), High Voltage Alternating Current (HVAC), Power frequency and High frequency: Impulse voltage and impulse current Generation, Tripping and contact of Impulse Generator. Measurement of voltage and current: High voltage direct current, Alternating current and Impulse voltage and currents.

BOOKS RECOMMENDED:

1. Bagamudre, Rakesh Das *Extra High Voltage A.C. Transmission Engineering*, New Age International Publishers.
2. Kimbark E.W., *High Voltage Direct Current Transmission*, Wiley-Interscience
3. Kamaraju V. and Naidu M.S., *High Voltage Engineering*, Tata McGraw-Hill Education
4. Jha R.S., *High Voltage Engineering*, Dhanpat Rai
5. Kuffel, E. and Abdullah, M., *High Voltage Engineering*, Pergamon Press
6. Wadhwa C. L., *High Voltage Engineering*, New Age Publications.
7. Padiyar, K.R. *HVDC Power Transmission Systems: Technology and System Interactions*, New Age International

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-804C/BTEE-804C DIGITAL SIGNAL PROCESSING

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction: Signals, Systems and Signal processing, Classification of Signals, Concept of frequency in continuous time and discrete time signals.

Discrete Time Signals and Systems: Discrete time signals, Discrete time systems, Analysis of discrete time linear time-invariant systems, Discrete time systems described by difference equations, Implementation of discrete system, Correlation of discrete time signals.

Z-Transform: The Z-transformation, properties of Z-transformation, Rational Z-transformation, Inversion of Z-transform, Analysis of linear time invariant systems in Zdomain.

Frequency Analysis of Signals and Systems: Frequency analysis of continuous time signals, Frequency analysis of discrete time signals, Properties of Fourier Transform for discrete time signals, Frequency domain characteristics of linear time invariant systems, linear invariant systems as frequency selective filters, Inverse systems and de-convolution.

The Discrete Fourier Transform: Frequency domain sampling, Properties of Discrete Fourier Transform (DFT), Linear filtering methods based on DFT, Frequency analysis of signals using the DFT.

Design of Digital Filters: General considerations, Design of Finite Impulse Response (FIR) filters, Design of Infinite Impulse Response (IIR) filters from analog filters, Frequency transformations, Design of digital filters based on least-square method and window method, Comparison of IIR and FIR filters.

BOOKS RECOMMENDED:

1. Oppenheim A.V. and Schafer, R.W., *Digital Signal Processing*, Prentice Hall (India)
2. Kuo, Sen-Maw and Gan, Woon-Seng, *Digital Signal Processing architectures, Implementations, and Applications* McGraw Hill
3. Proakis John G., *Digital Signal Processing: Principles, Algorithms, and Applications*, Pearson Education 4th Ed. (2007)
4. Richard G Lyons, *Understanding Digital Signal Processing*, Pearson Education Publications.
5. Mitra K. Sanjit, *Digital Signal Processing*, 3rd ed. Tata McGraw Hill
6. Hayes Mansen, *Schaum's Outline of Digital Signal Processing*, Tata McGraw Hill, (2001)

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-804D/BTEE-804D INDUSTRIAL PROCESS CONTROL

Internal Marks:	40	L	T	P
External Marks:	60	3	1	0
Total Marks:	100			

Description And Modeling of Various Industrial Processes: Model Classification, Mathematical Models, Physical Models, Analog Models, Estimation of Model Parameters, System Identification, Experimental Nature of Simulation, Steps Involved in Simulation Studies, Validation of Simulation Models, Computer Simulation of Continuous and Discrete Systems, Examples

Process Control: Types and Description of Processes, Blending, batch processes, compressor and chiller controls, distillation control, steam turbine and water treatment controls, boiler controls, reactor controls

Conventional Controllers: On-off Controllers, Cascade and Feed forward Controllers, Split Range Controllers, ratio controls, Single loop, multi loop and self tuning controllers, set point control (SPC), discrete digital control (DDC)

Intelligent Controllers: Fuzzy logic control, programmable logic controllers, PC based system, conventional and Windows-NT based distributed control system (DCS) systems, artificial intelligence and neural networks, smart and intelligent transmitters.

BOOKS RECOMMENDED:

5. Padmanabhan, Tattamangalam R, *Industrial Process Instrumentation and control* Springer Publishing
6. Andrew W.G. and Williams H.B., *Applied Instrumentation in the Process Industries*, Gulf Publishing, Houston
7. Nolting B.E., *Instrumentation Reference Book*, Elsevier India Pvt, New Delhi
8. Liptak B.G., *Instrument Engineer's Handbook (Process Control)*, Elsevier India Pvt, New Delhi.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-804E/BTEE-804E NETWORKS AND DATA COMMUNICATION

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction: Basic Concepts of analog and digital signals, data transmission concepts, Analog and digital transmission, transmission impairments

Transmission Media: Guided and Un-guided media, Performance, Shannon Capacity, Media Computerization

Encoding and Modulating: Digital-to-Digital conversion, Analog and digital conversion, Digital to Analog conversion, Analog to Analog conversion

Digital Data Communication: Digital data transmission, Data terminal equipment (DTE) - data circuit-terminating equipment (DCE) Interface, Electronic Industries Alliance (EIA)-449, EIA- 530, X.21 (Communication standard), Modems, Cable Modems

Multiplexing And Switching: Frequency-division multiplexing (FDM), wavelength-division multiplexing (WDM), Time-division Multiplexing (TDM) application- telephone systems, Digital subscriber line (DSL), Par Circuit switching , Packet Switching and Message switching virtual circuits

Spread Spectrum: Concept, Frequency hopping spread spectrum, direct sequence spread spectrum, code division Multiple Access

Error Detection and Correction: Types of Errors, Detection, Vertical Redundancy Check (VRC), longitudinal redundancy check (LRC), cyclic redundancy check (CRC), Checksum, Error Correction

Protocol Architecture: Protocols, Standards, OSI (Open Systems Interconnection) model, TCP (Transmission Control Protocol)/ IP (Internet Protocol) Protocol Architecture

BOOKS RECOMMENDED:

1. Ulyers D Balck, *Data Communication and Distributed Networks*, Prentice Hall (India)
2. Andrew S. Teanebaum, *Computer Networks*, Prentice Hall (India)
3. William Stallings, *Data and Computer Communication*, Pearson Education
4. Behrouz A Ferouzan *Data Communications and Networking*, Tata McGraw Hill.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-804F/BTEE-804F DATA MINING AND PATTERN RECOGNITION

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Data Mining: What is data mining, on what kind of data, Data Mining Functionalities

Data Warehouse: Difference Between operational database systems and data warehouses, A multidimensional data model, Data Warehouse architecture, data warehouse architecture, Data Warehouse implementation.

Data preprocessing: Data cleaning, data integration and transformation, data reduction.

Data Mining Query Language: Characterization and Comparison, Generalization, Mining association rules in large databases, constraint based association Mining Classification and prediction Classification by decision Tree Induction, Bayesian classification, classification by Back propagation Cluster analysis Partitioning Methods, Hierarchical methods, and Density and Grid based methods, Mining complex types of data, applications and trends in data mining, Social impacts of data mining.

Pattern recognition: Its importance and applications, applications in Bioinformatics, recognizing important bio-informatics sequences, other applications of pattern discovery.

Laboratory Work: Implementation of various data mining techniques like classification, clustering, generalization, cleaning etc.

BOOKS RECOMMENDED:.

1. Pal. Sankar K. and Mitra P., *Pattern Recognition Algorithms for Data Mining*, Chapman and Hall/CRC
2. Elden L. *Matrix Methods in Data Mining and Pattern Recognition*. SIAM, 2007.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-805A/BTEE-805A POWER QUALITY MONITORING AND CONDITIONING

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Overview and definition of power quality (PQ): Sources of pollution, and regulations, Power quality problems rapid voltage fluctuations voltage unbalance, Voltage dips and voltage swells, Short duration outages,

Definitions Voltage sag analysis and mitigation: Sag caused by motor starting, Sag caused by utility fault clearing, Sag mitigation, Sag magnitude and duration calculations in single-phase systems, Equipment performance in presence of sag, Computers, Alternating current (AC) and direct current (DC) drives.

Harmonics: Effects-within the power system, Interference with communication Harmonic measurements. Harmonic elimination.

Harmonic distortion: Power system harmonics: harmonic analysis, Harmonic sources-the static converters, Transformer magnetization and non-linearities, Rotating machines, arc furnaces, Fluorescent lighting. Introduction to power converters, Fourier analysis, Total harmonic distortion, rms and average value calculations, Arcing and saturable devices, Effects of harmonic distortion, System response characteristics.

Principles for controlling harmonics: Locating sources of harmonics, Passive and active filters, Harmonic filter design.

Monitoring power quality: Monitoring essentials, Power quality measuring equipment, Current industry trends.

Power Conditioning: Electric power conditioning, Active and passive filters, IEEE, IEC, ANSI standards, Power Acceptability Curves, Various standards

BOOKS RECOMMENDED:

1. Beaty, H. and Santoso, S., *Electrical Power System Quality*, McGraw-Hill (2002).
2. Kennedy, B., *Power Quality Primer*, McGraw Hill (2000).
3. Bollen, M.H.J., *Power Quality Problems: Voltage Sag and Interruptions*, IEEE Press (2007).
4. Mohan, N., *Power Electronics*, New Age International (P) Limited, Publishers (2007).

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-805B/BTEE-605A COMPUTER AIDED ELECTRICAL MACHINE DESIGN

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Review of Magnetic and insulating materials.

Principles of design of Machines: Factors and limitations in design, specific magnetic and electric loadings, output, real and apparent flux densities, separation of main dimensions for D.C., induction and synchronous machines.

Heating, Cooling and Ventilation: Temperature rise calculation, continuous, short time and intermittent ratings, types of ventilation, hydrogen cooling and its advantages.

Design of Transformers: General considerations, output equation, main dimensions, leakage reactance, winding design, tank and cooling tubes, calculation of magnetizing current, losses, efficiency and regulation.

Design Three-phase induction motors: General considerations, output equation, choice of specific electric and magnetic loadings, No. of slots in stator and rotor, elimination of harmonic torques, design of stator and rotor windings, leakage reactance, equivalent resistance of squirrel cage rotor, magnetizing current, temperature rise and efficiency.

Introduction to computer aided electrical machine design.

BOOKS RECOMMENDED:

1. Sawhney A.K., *A Course in Electrical Machine Design*, Dhanpat Rai.
2. Aggarwal R.K., *Principles of Electrical Machine Design*, S. K. Kataria and Sons.
3. Deshpande M.V., *Design And Testing of Electrical Machines*- PHI Learning Pvt. Ltd.
4. Upadhyay K.G., *Design of Electrical Machine*, New Age International.
5. Hamdi Essam S., *Design of small electrical machines*, Wiley publications

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-805C/ BTEE-805C VIRTUAL INSTRUMENTATION

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction: Virtual Instrumentation - Definition, flexibility, Block diagram and Architecture of Virtual Instruments, Virtual Instruments versus Traditional Instruments, Review of LABVIEW software in virtual Instrumentation and programming techniques.

Data Acquisition In Virtual Instrumentation: Analog-to-Digital, Digital-to-Analog converters, plug-in Analog input/output cards, Digital Input/ Output cards, Organization of the Data acquisition (DAQ)-VI system, Opto-isolation, Performing analog input and analog output, Scanning multiple analog channels, Issues involved in selection of data acquisition cards, Data acquisition modules with serial communication.

Communication Networked Modules: Introduction to Personal Computer (PC) Busses, Local busses: Industry Standard Architecture (ISA), Peripheral Component Interconnect (PCI), RS232, RS422, RS485, Interface Busses, Universal Serial Bus (USB), Personal Computer Memory Card International Association (PCMCIA), Virtualization eXperience Infrastructure (VXI), Signal Conditioning, eXtensions for Instrumentation (SCXI), PCI eXtensions for Instrumentation (PXI). Instrumentation Buses: Modbus, General Purpose Interface Bus (GPIB) Networked busses, ISO (International Organization for Standardization)/OSI (Open Systems Interconnection) Reference model, Ethernet TCP (Transmission Control Protocol)/ IP (Internet Protocol) protocols.

Real Time Control in Virtual Instrumentation and Applications: Design of ON/OFF controller, simulation of industrial instruments and systems, Virtual Instrumentation functions and objects including signal processing and analysis. Typical instruments and systems -digital storage oscilloscope, spectrum analyzer, waveform generator, Data visualization from multiple locations; Distributed monitoring and control devices.

BOOKS RECOMMENDED:

1. Wells L. K. and Travis J., *Labview for everyone*, Prentice Hall
2. Gupta S. and Gupta J.P., *PC interfacing for data acquisition and process control*, ISA
3. Rahman Jamal and Herbert Picklik, *Labview — Applications and solutions*, National Instruments Release
4. Gary Jhonson, *Labview Graphical programming*, McGraw Hill

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-805D/BTEE-805D ENERGY EFFICIENT MACHINES

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

INTRODUCTION: Need for energy efficient machines, energy cost and two part tariff, energy conservation in industries and farms -a necessity, introduction to energy management and energy audit system. Review of induction motor characteristics.

ENERGY EFFICIENT MOTORS: Standard motor efficiency, why more efficient motors? An energy efficient motor, efficiency determination methods, Direct Measurement method, Loss segregation method, Comparison, motor efficiency labelling, energy efficient motor standards. Motor life cycle

POWER FACTOR: The power factor in sinusoidal systems, power factor improvement, power factor with nonlinear loads, Harmonics and the power factor

INDUCTION MOTORS AND ADJUSTABLE DRIVE SYSTEMS: Energy Conservation, adjustable speed systems, Application of adjustable speed systems to fans, pumps and constant torque loads.

BOOKS RECOMMENDED:

1. Andreas John C., *Energy efficient electric motors*, Marcel Dekker Inc. 1992.
2. Thuman Albert, *Introduction to Efficient Electric System Design*, The Fairmount Press Prentice Hall.
3. Tripathi S.C. , *Electric Energy Utilization and Conservation*, Tata McGraw-Hill 1991.
4. Belove Charles, *Handbook of Modern Electronics and Electrical Engineering*, John Wiley & Sons

BTEIC-805E/BTEE-805E EMBEDDED SYSTEM

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction: Review of Embedded Hardware: Terminology, Gates, Timing Diagram, Memory, Microprocessor Buses, Direct Memory Access, Interrupts, Built instructions on the Microprocessor. Conventions used on Schematic, Interrupts, Microprocessor Architecture, Interrupt Basic, Shared Data Problem, Interrupt Latency.

PIC Micro controller and Interfacing: Introduction, CPU Architecture, Registers, Instruction Sets, Addressing Modes, Programs, Interfacing Methods, Parallel I/O Interface, Parallel Port Interface, Memory Interfacing, High Speed I/O Interfacing, Interrupt, Interrupt Service Routine, features of Interrupts, Interrupt vector and Priority, Timing Generation and Measurements, Input Capture, Output Compare, Frequency Measurement, Serial I/O Device RS232, RS485, Analog Interfacing, Applications.

Software Development and Tools: Embedded System Evolution Trends, Round – Robin, Robin with Interrupts, Function Scheduling architecture, Algorithms, Introduction to assembler, Compiler and Cross compilers and Integrated Development Environment (IDE), Object Oriented Interfacing, Recursion, Debugging Strategies, Simulators.

Real Time Operating Systems (RTOS): Task And Task States, Tasks and Data, Semaphores and shared data, operating system services, Message queues, Timer Function, Events, Memory Management, Interrupt Routines in an RTOS Environment, Basic Design Using RTOS.

BOOKS RECOMMENDED:

1. Gajski D.D., Vahid F., Gong J., Narayan S., *Specification and Design of Embedded Systems*, Prentice Hall.
2. Steve Heath, Newnes *Embedded systems Design*, Prentice Hall.
3. Balarin F., Chiodo, *Hardware Software Co-design of Embedded Systems*, Academic Publishers.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-805F/BTEE-805F VISUAL PROGRAMMING

Internal Marks: 40
External Marks: 60
Total Marks: 100

L	T	P
3	1	0

Introduction: Visual programming basics, Application framework fundamental, windows programming (Win32 programming), and Visual C++.memory management, Dynamic-link library (DLLs), Win32, Application programming interface (API).

Visual C++ components: Resource compiler, Microsoft Foundation Classes (MFC), modal Dialog, Windows Common Control, the Modeless Dialog and Windows Common Dialogs, ActiveX Control, Bitmap, Reading and Writing Documents, Serial digital interface (SDI), multiple document interface (MDI) applications.

Socket Programming using Win Sock, TCP/IP (TCP (Transmission Control Protocol/Internet Protocol), Document-View Structure, Microsoft Foundation Class (MFC) Libraries viz Cview, Cfile, Cpoint, Cdialog.

WIN32 Programming: WIN32 programming, Difference between a Windows program and a typical DOS program, Windows Programming modal, Windows Memory management, A skeletal Windows Application: WinMain function, Window Function, Components of a Skeletal Application, Windows style, Device context, Creation of LISTBOX class, Dialog Boxes and SCROLLBAR class.

BOOKS RECOMMENDED:

1. David J. Kruglenski *VISUAL C++ programming*, Microsoft Press
2. Newcomer, Addison, *WIN32 Programming*, Wesley
3. Petzold, Charles, *Programming Windows 3.1*, Microsoft press.

Note: External question paper shall be set following guidelines to paper setter given at Page 59.

BTEIC-802 Laboratory-XIII (Simulation)

Internal Marks: 30
External Marks: 20
Total Marks: 50

L	T	P
0	0	2

Data Flow Programming Techniques: Graphical programming in data flow, comparison with conventional programming, popular data flow and VI software packages. Building a VI front panel and block diagram, sub VI, for and while loops, case and sequence structure, formula nodes, local and global, string and file I/O, array and clusters, charts and graphs, attributes nodes.

Data Acquisition Basics: ADC, DAC, D/O, counters and timer, PC hardware structure, timing, interrupts, DMA, software and hardware installation, Configuring data acquisition hardware using the drives in application software, use of DAQ library functions for different analog and digital input/output operations.

Common Instrument Interfaces: Current loop, RS 232, RS485, GBIP. Use of library functions to communicate with different instruments.

Use of Measurement Analysis Tools: Measurement of Max, Min, Peak-Peak voltage, Mathematical tools, time period of a signal, power spectrum and logging Fourier transform, Correlation methods, windowing and filtering.

Building a web based virtual instrument: Networking basics for office and industry application.

BOOKS RECOMMENDED:

1. Gupta, *Virtual Instrumentation Using Labview*, Tata McGraw Hill
2. S Gupta & J Gupta, *PC Inerfacing for data acquisition*, ISA
3. Wells Lisa K, Travis Jeffry, *LabVIEW for everyone*, Prentice Hall (India)
4. Johnson Gary W, *Lab view Graphical Programming*, McGraw Hill

BTEIC-807 PROJECT WORK

Internal Marks: 60
External Marks: 40
Total Marks: 100

L	T	P
0	0	6

Design, Fabrication, Simulation, Evaluation, Testing etc. related to Electrical Engineering is to be carried out under the supervision of guide(s).

BTEIC-808 SEMINAR

Internal Marks: 100
External Marks:
Total Marks: 100

L	T	P
0	0	2

Students will be required to prepare a report on a given topic related to latest developments in electrical engineering and deliver a seminar on that topic along with seminar report.

BTEIC-701 SOFTWARE TRAINING

Internal Marks: 150
External Marks: 100
Total Marks: 200

L	T	P
0	0	2

Students will be provided training on any of three of the programming language/ application softwares. All the applications shall be related to the Electrical components and systems.

- Any high level procedure oriented or object oriented programming language. Such language should be covered under regular or elective subject(s).
- MatLab
- LabView
- PSpice
- PSCAD

Students will undertake one project related to the Electrical components and systems based on the software training imparted during the semester in a group of three students. The entire group will select different projects. Students will be required to prepare a report on the Project undertaken and deliver a seminar on the project undertaken. The students will be evaluated based on Project undertaken, project report, seminar and viva-voce examination.

GUIDELINES TO PAPER SETTERS

1. The question paper shall have three sections:
Section A of 20 marks
Section B of 20 marks
Section C of 20 marks.
2. **Section A** is compulsory shall contain only **ONE** question with TEN sub-question carrying **two marks** each distributed from the entire syllabus. These questions shall be of conceptual nature and of short answer type to test the basic grasp of the subject matter by the students.
3. **Section B** shall contain **FIVE** questions and students shall be asked to answer any **FOUR** questions. Each question will carry **five marks**. These questions are to be set from different parts of syllabus with not more than one question from one part.
4. **Section C** shall contain **THREE** questions students shall be asked to answer any **TWO** questions. Each question shall carry **ten marks**. These questions are to be set from those parts of syllabus, which are not covered in Section B and restricted to not more than one question from each part.
5. At least 40% of the question should be numerical wherever applicable.
6. The paper setter shall provide detailed marking instructions and solution to numerical problems for evaluation purpose in the separate envelopes provided for solution.
7. The two different question papers should not contain more than 15% same/similar questions.